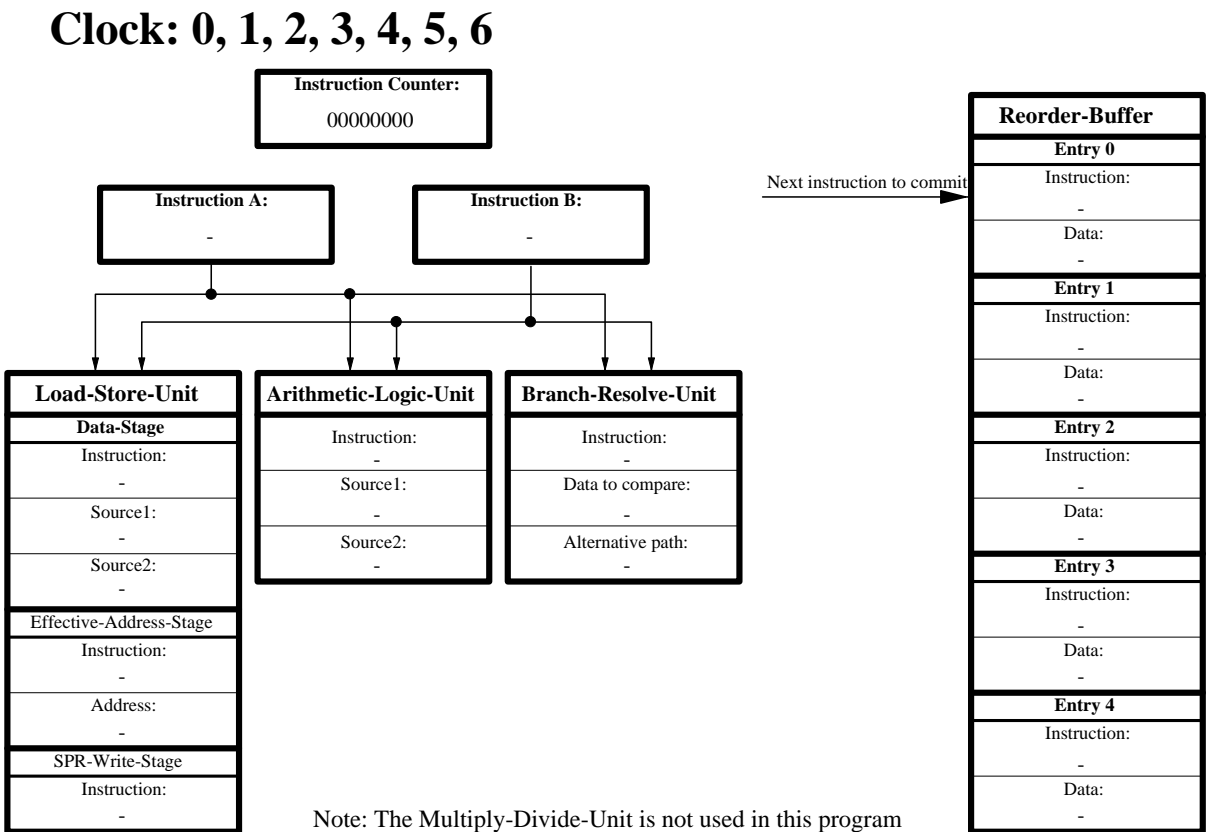
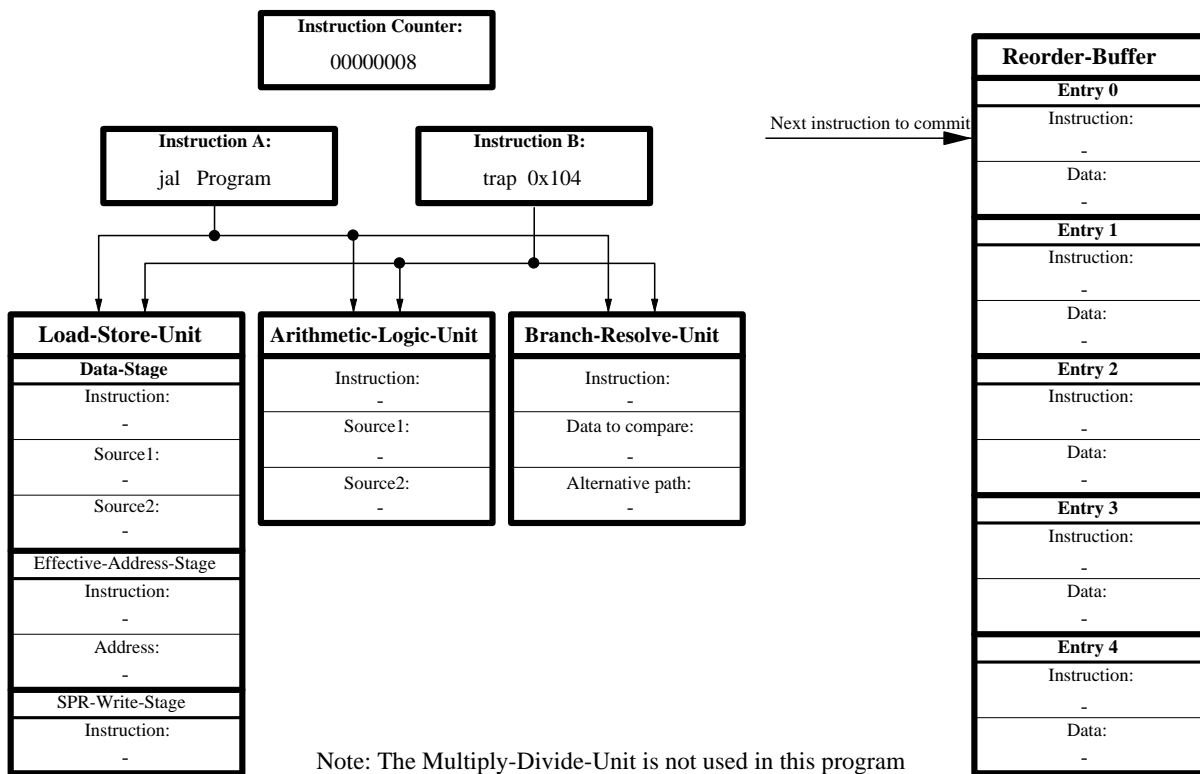


Bildliche Darstellung des Befehlsflusses durch die Pipeline

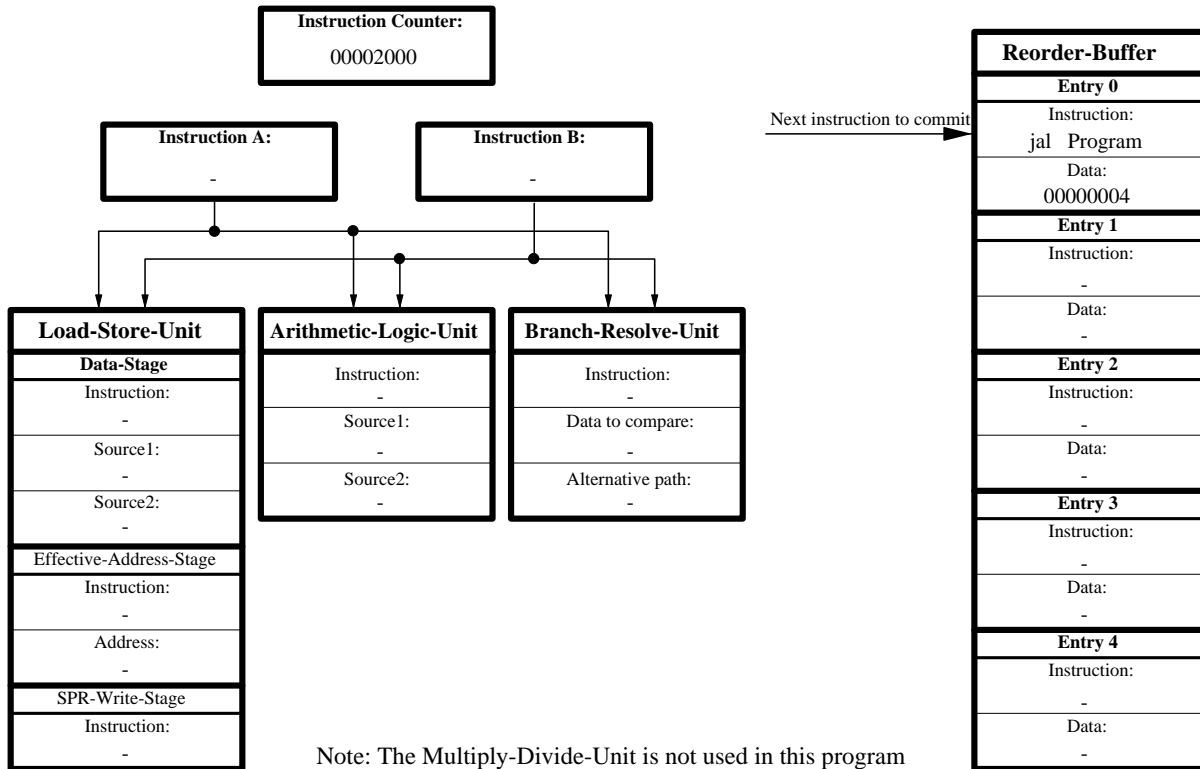
Die folgenden Abbildungen zeigen die wichtigsten Registerinhalte aus der Simulation des Programms 'Demo.asm'. Diese Darstellung soll helfen den Fluß der Befehle durch die Pipeline zu verfolgen.



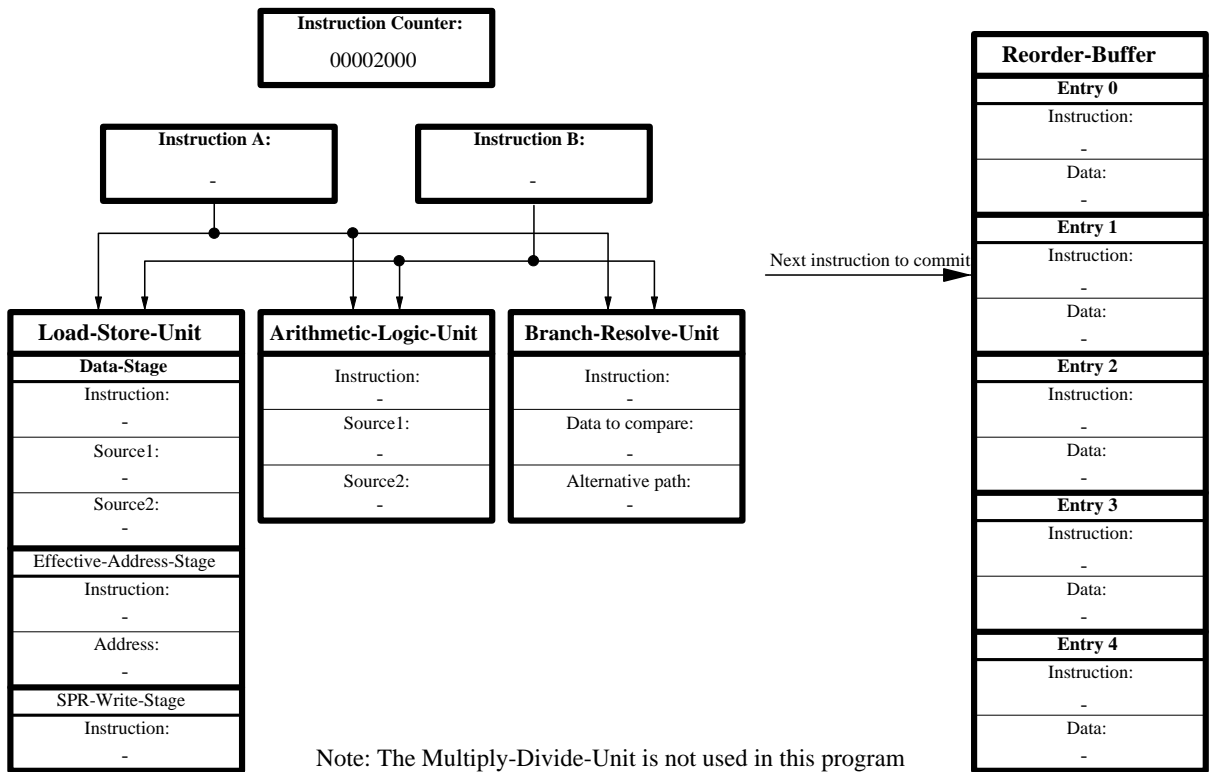
Clock: 7



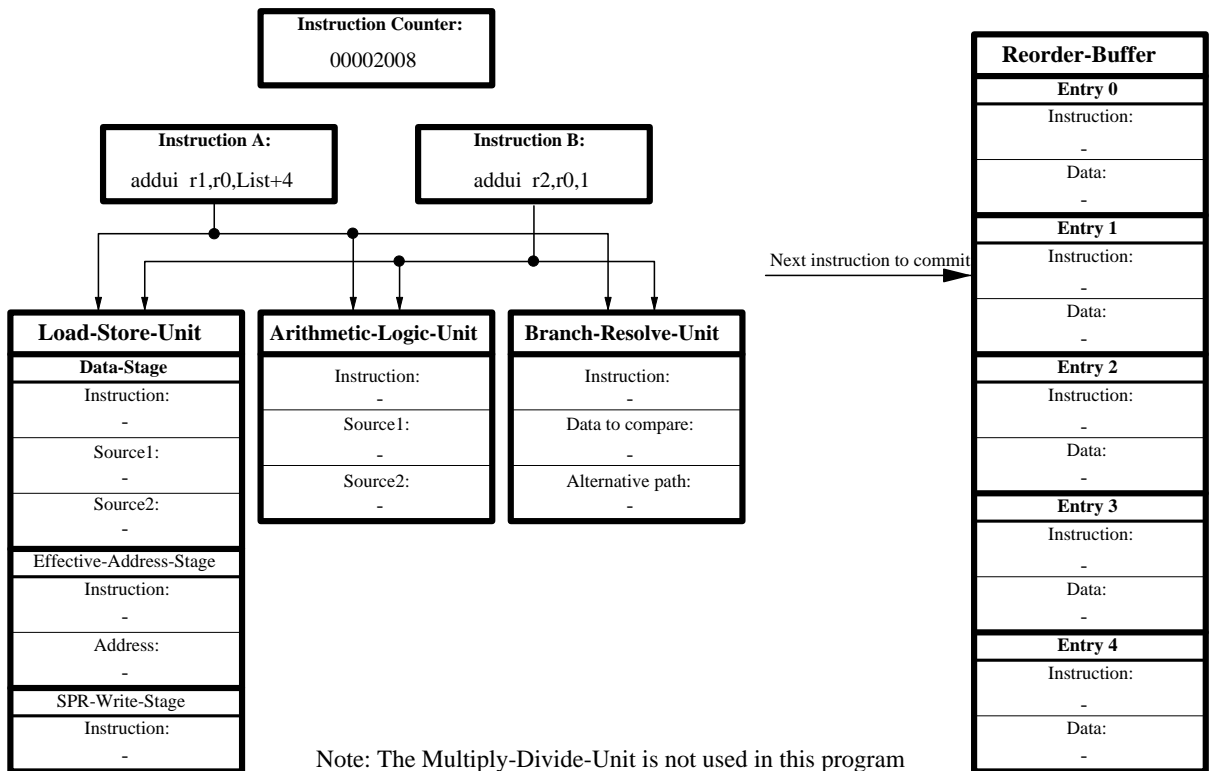
Clock: 8



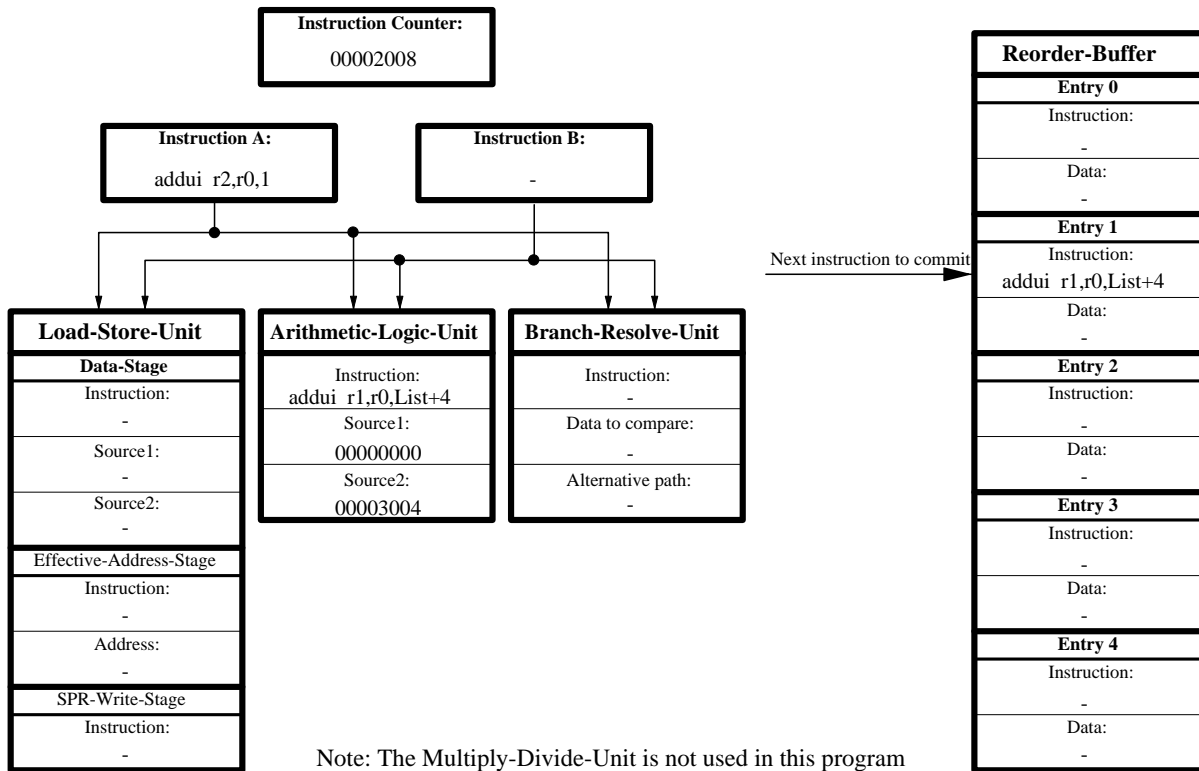
Clock: 9, 10, 11, 12



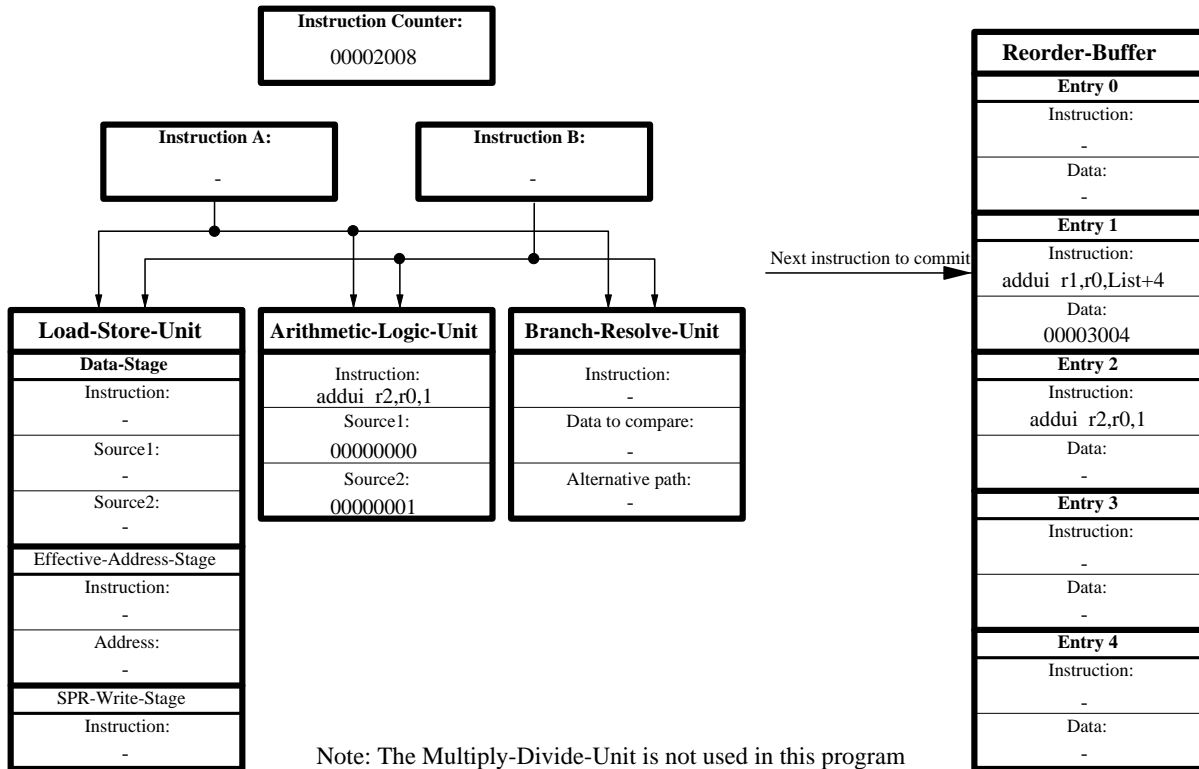
Clock: 13



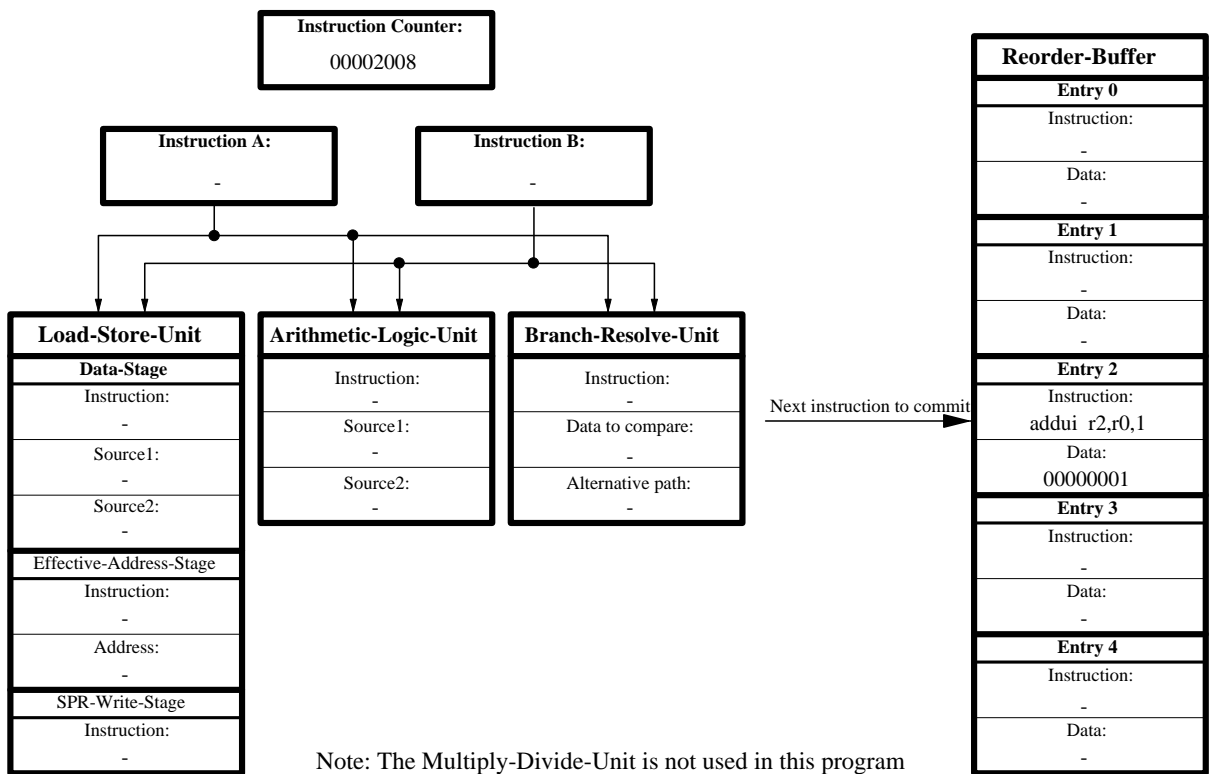
Clock: 14



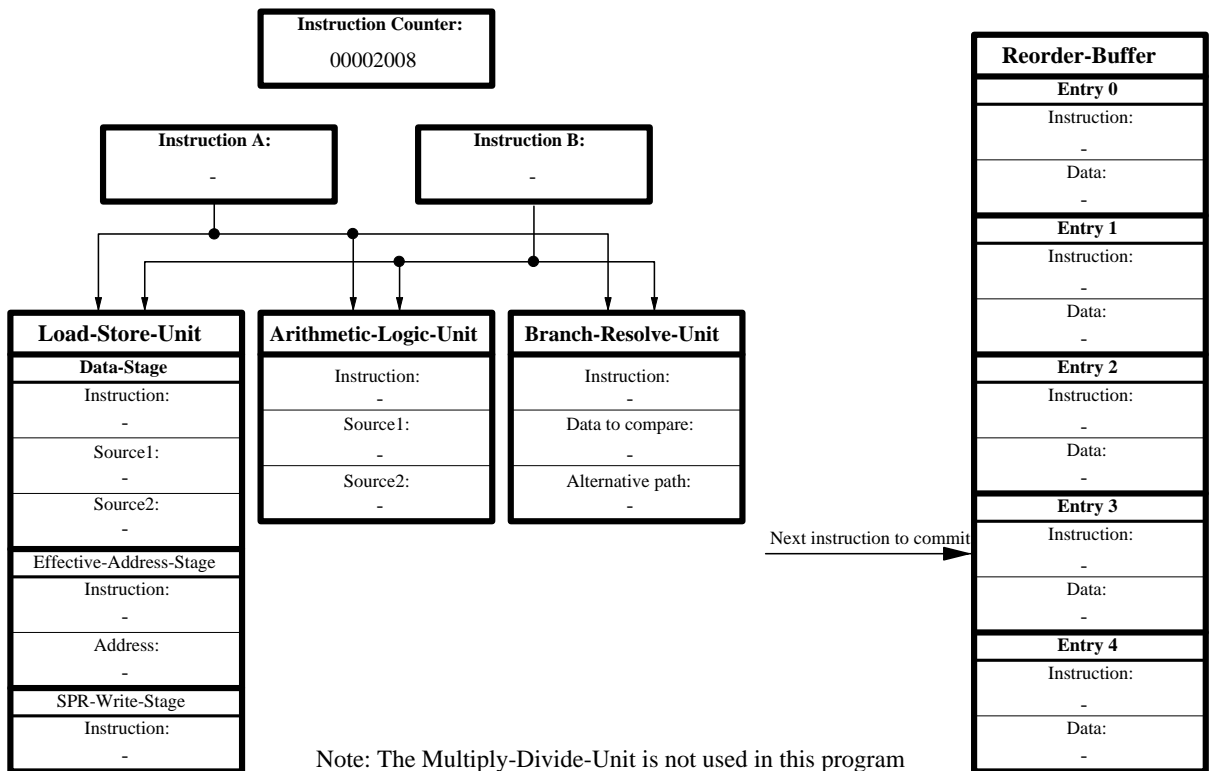
Clock: 15



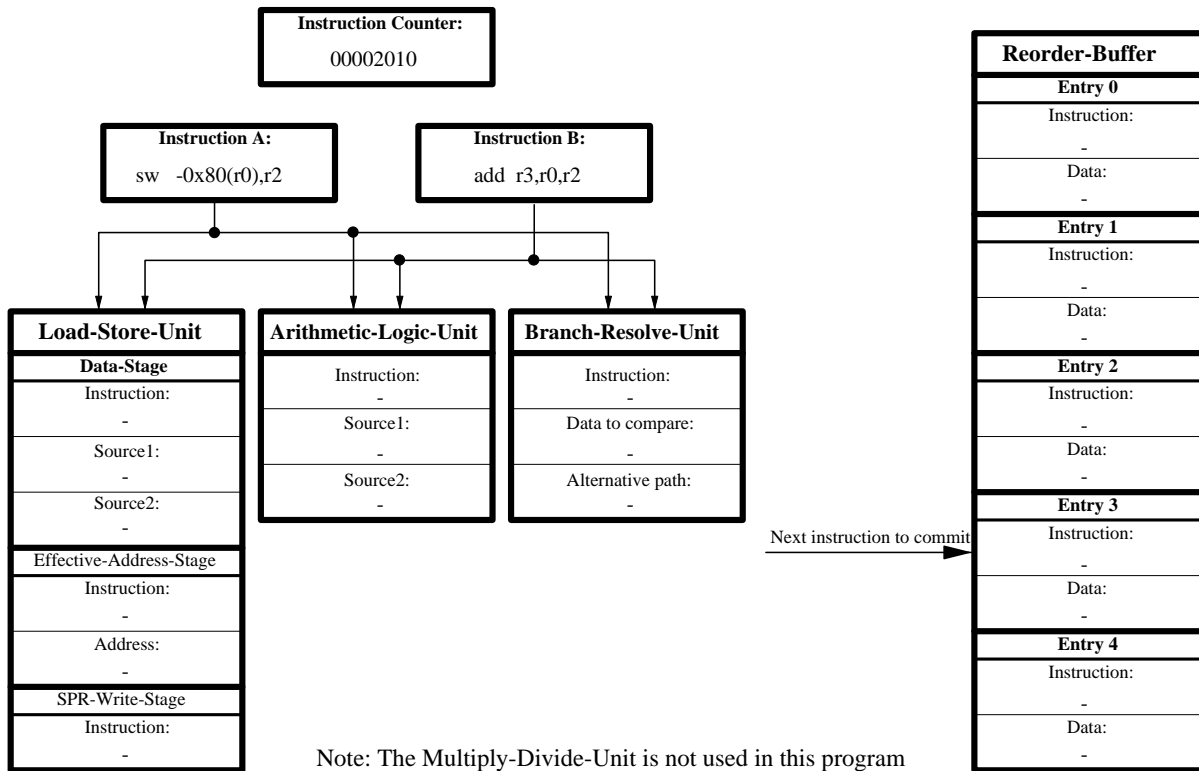
Clock: 16



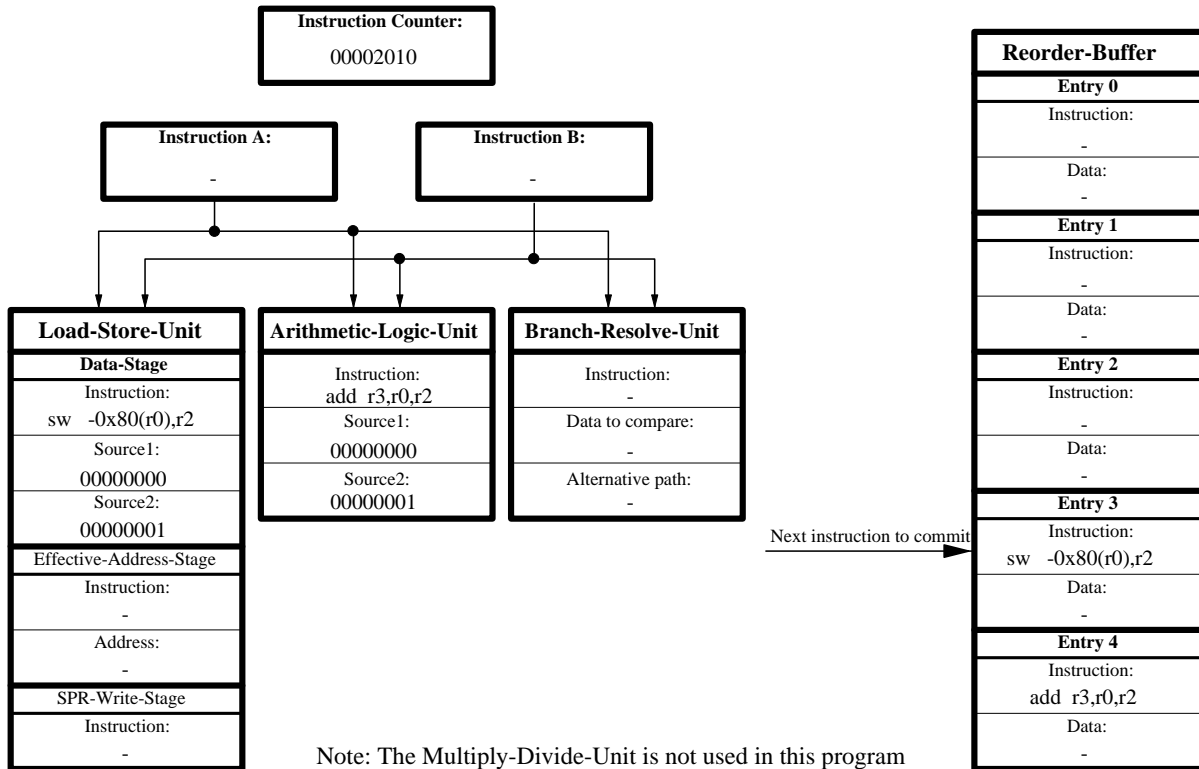
Clock: 17, 18



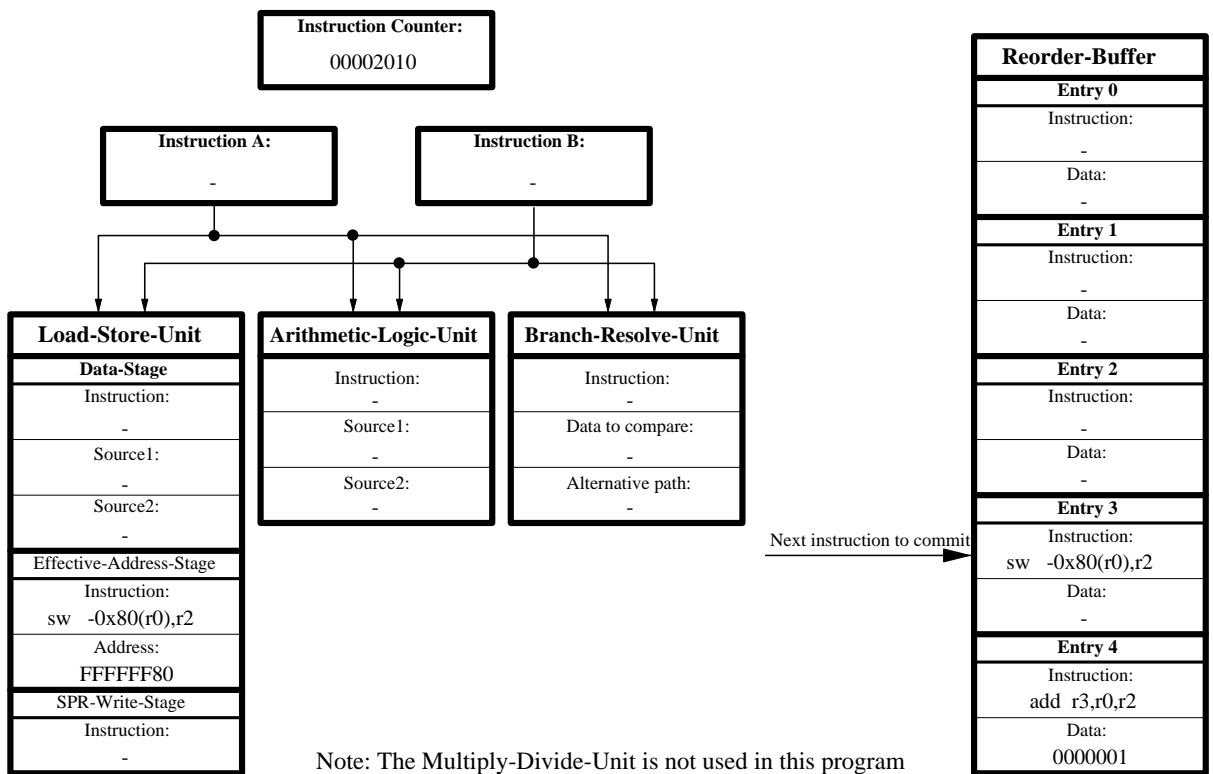
Clock: 19



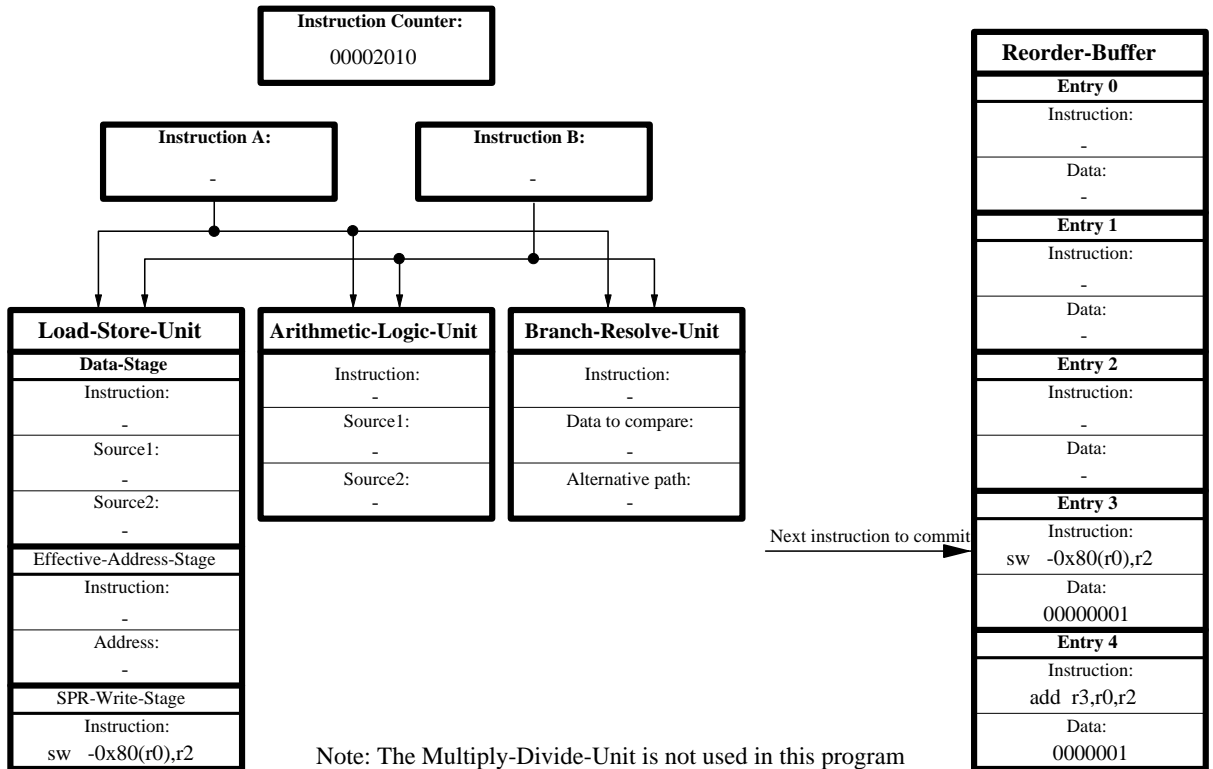
Clock: 20



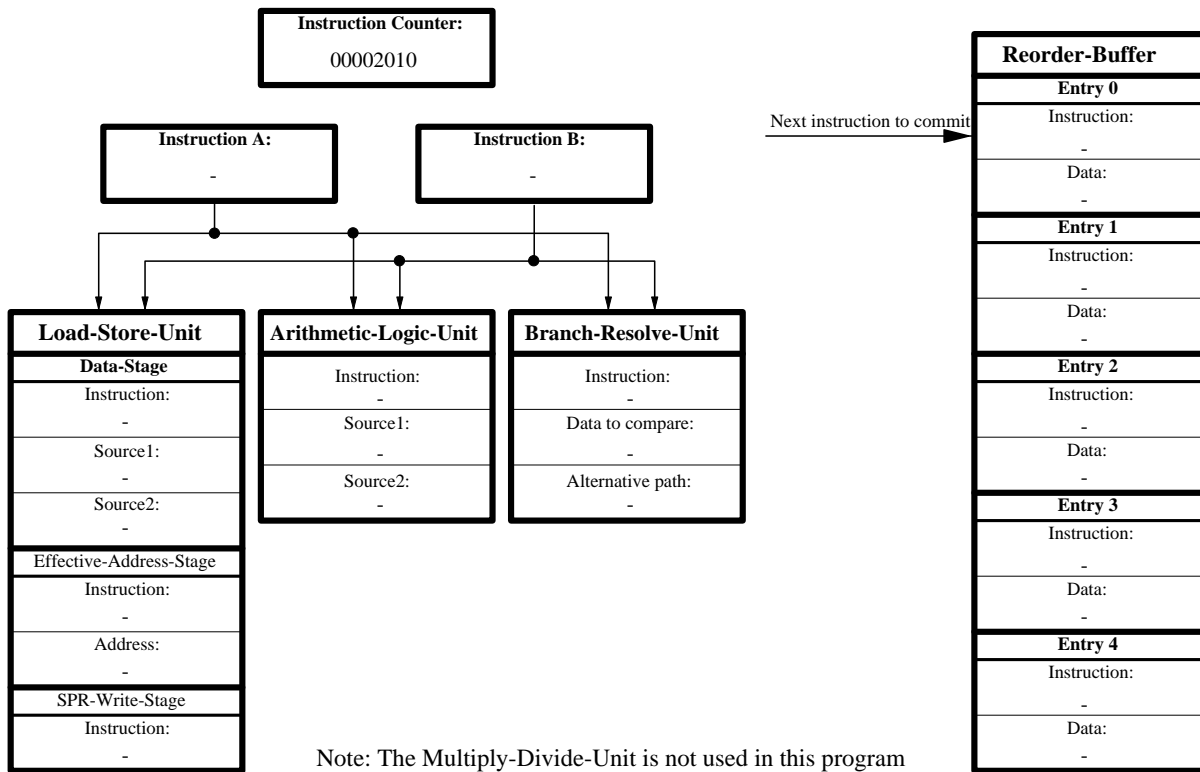
Clock: 21



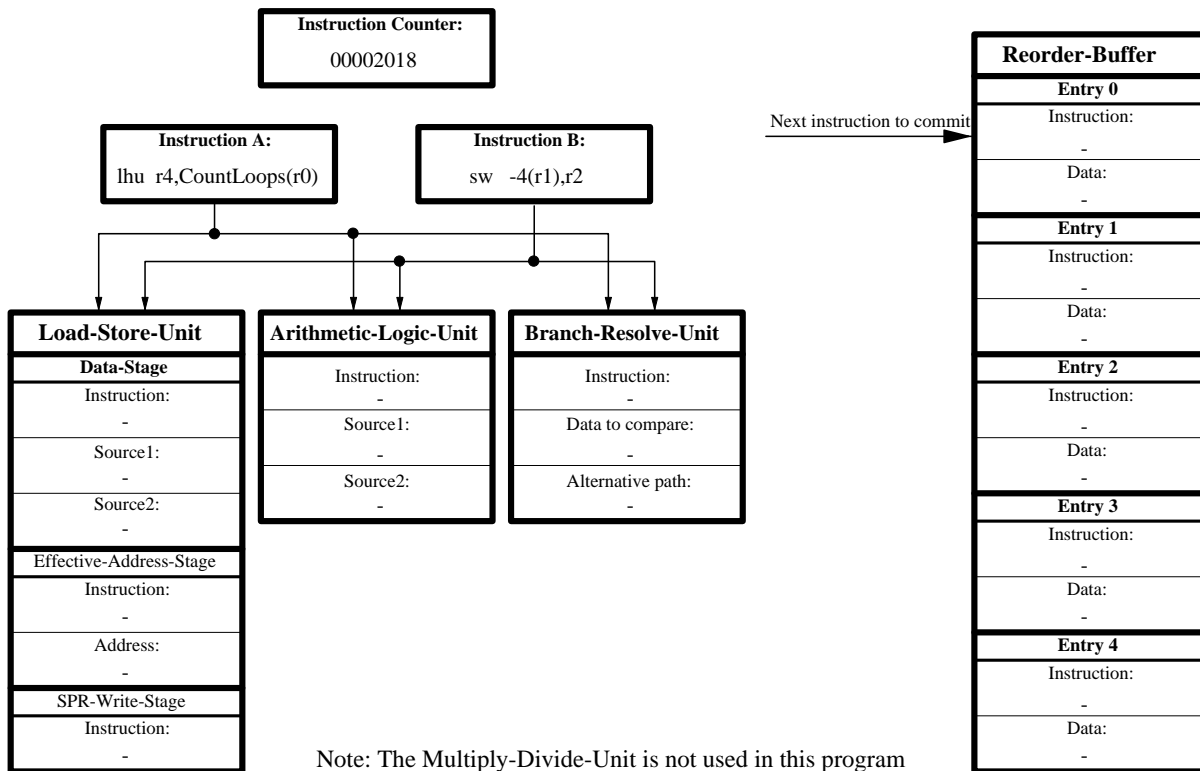
Clock: 22



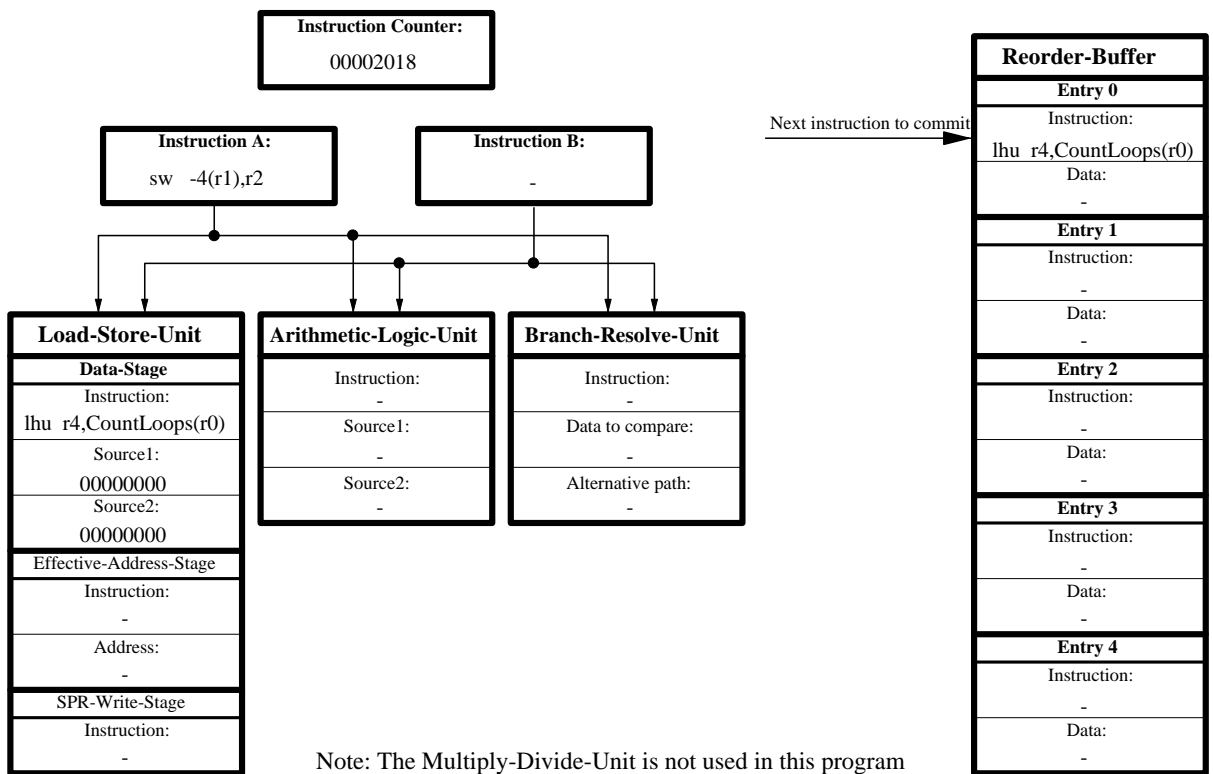
Clock: 23, 24



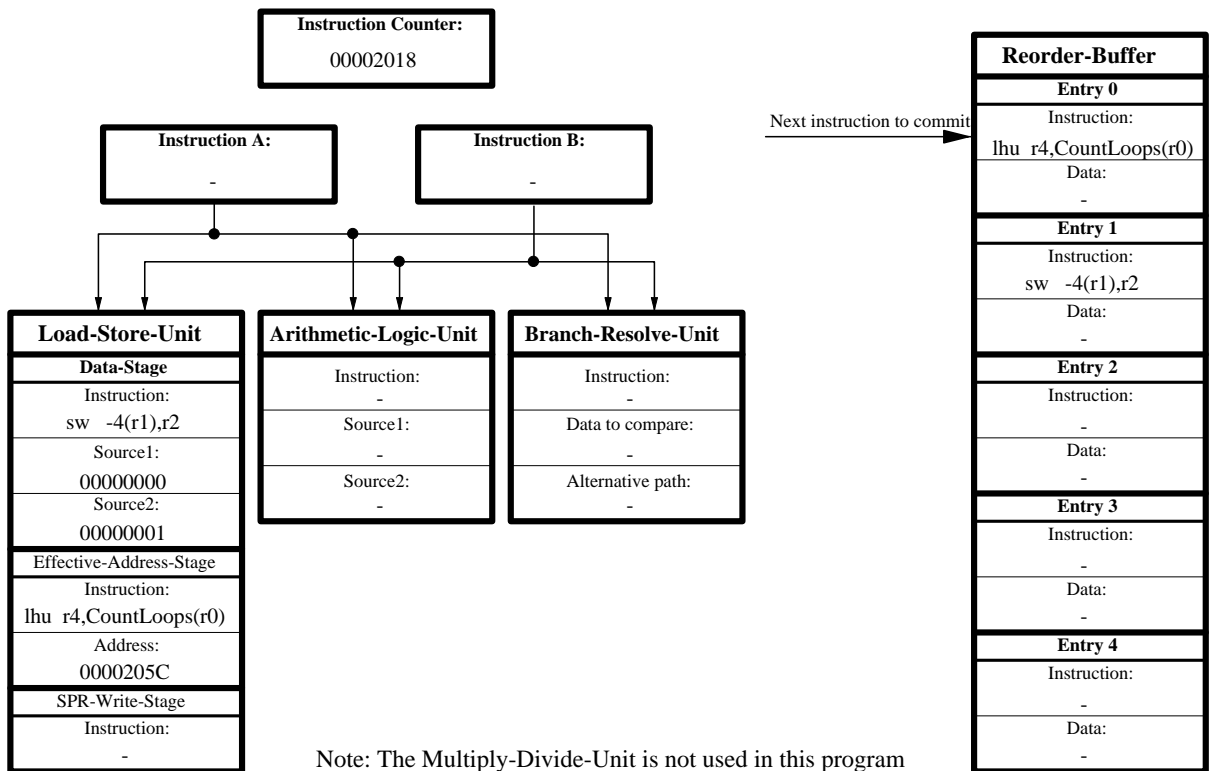
Clock: 25



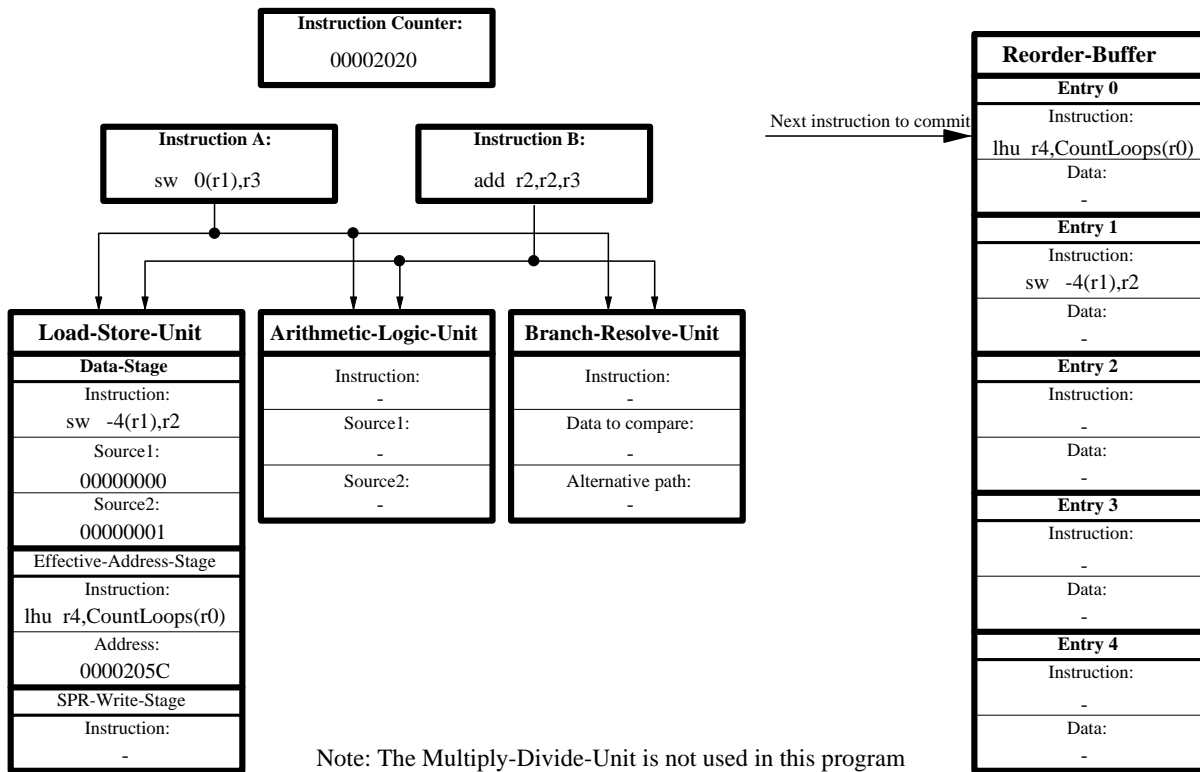
Clock: 26



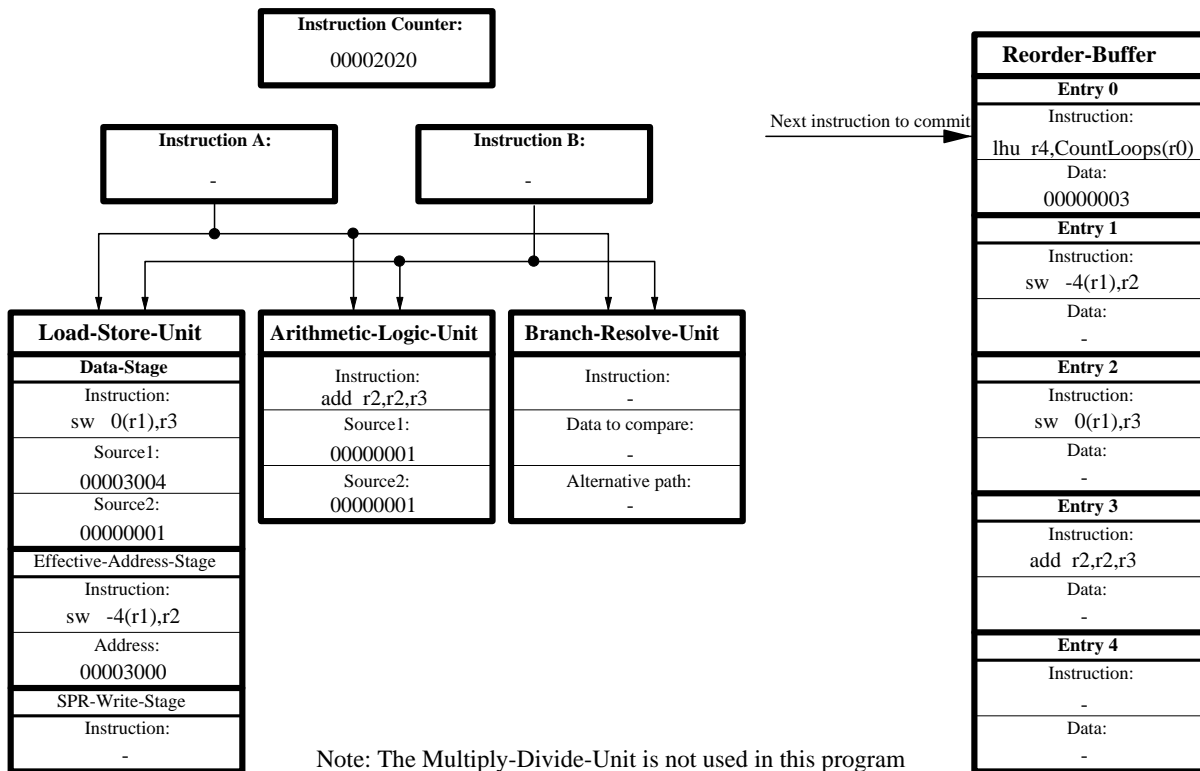
Clock: 27, 28, 29, 30



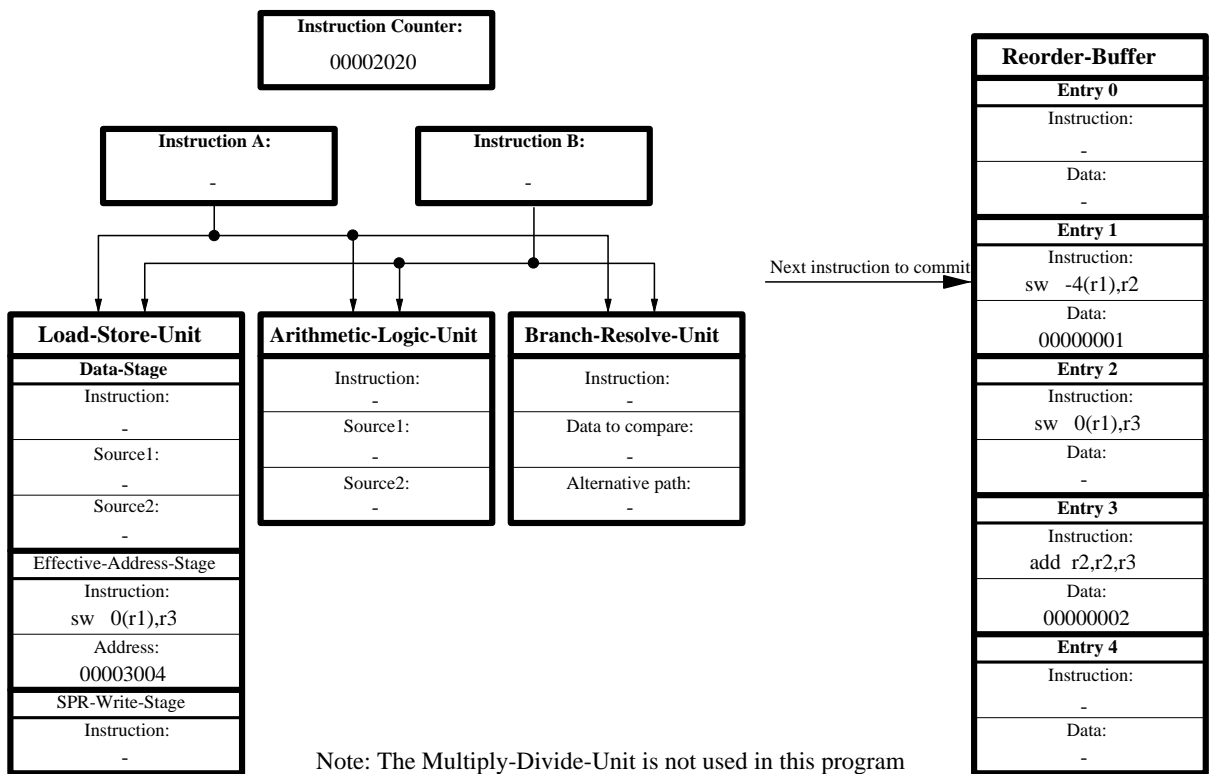
Clock: 31, 32, 33, 34



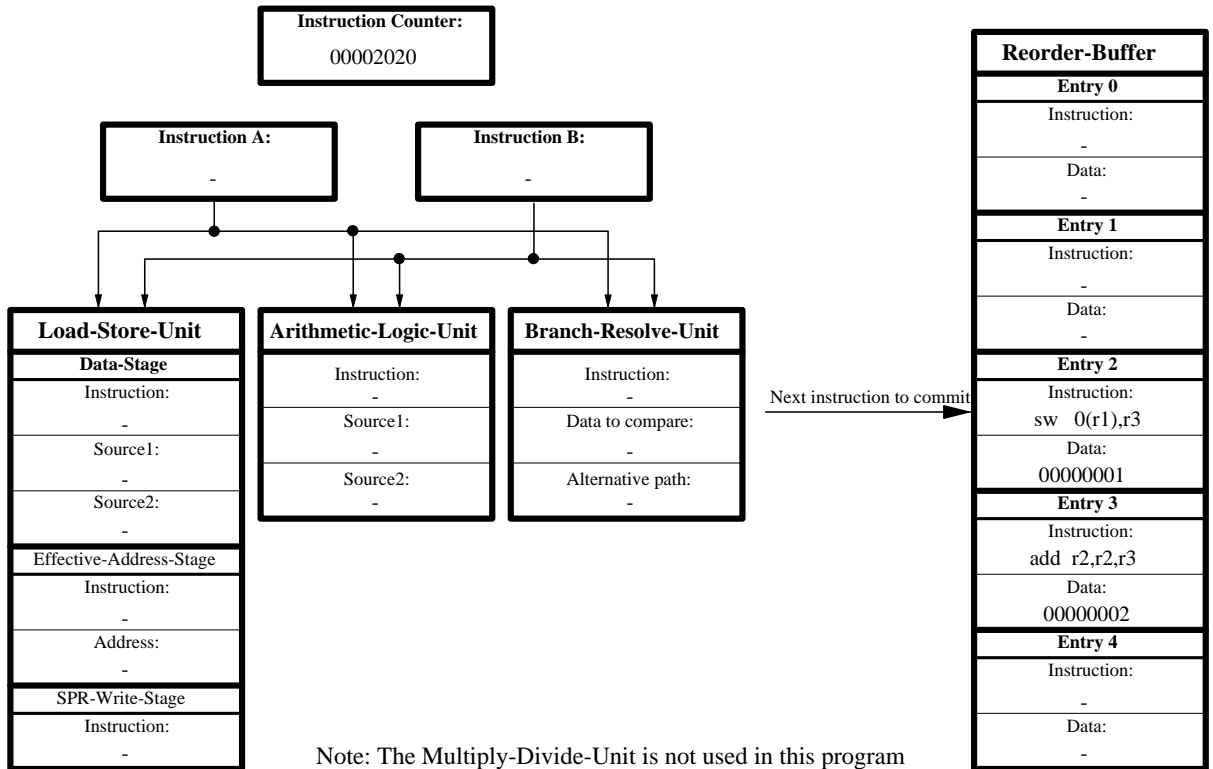
Clock: 35



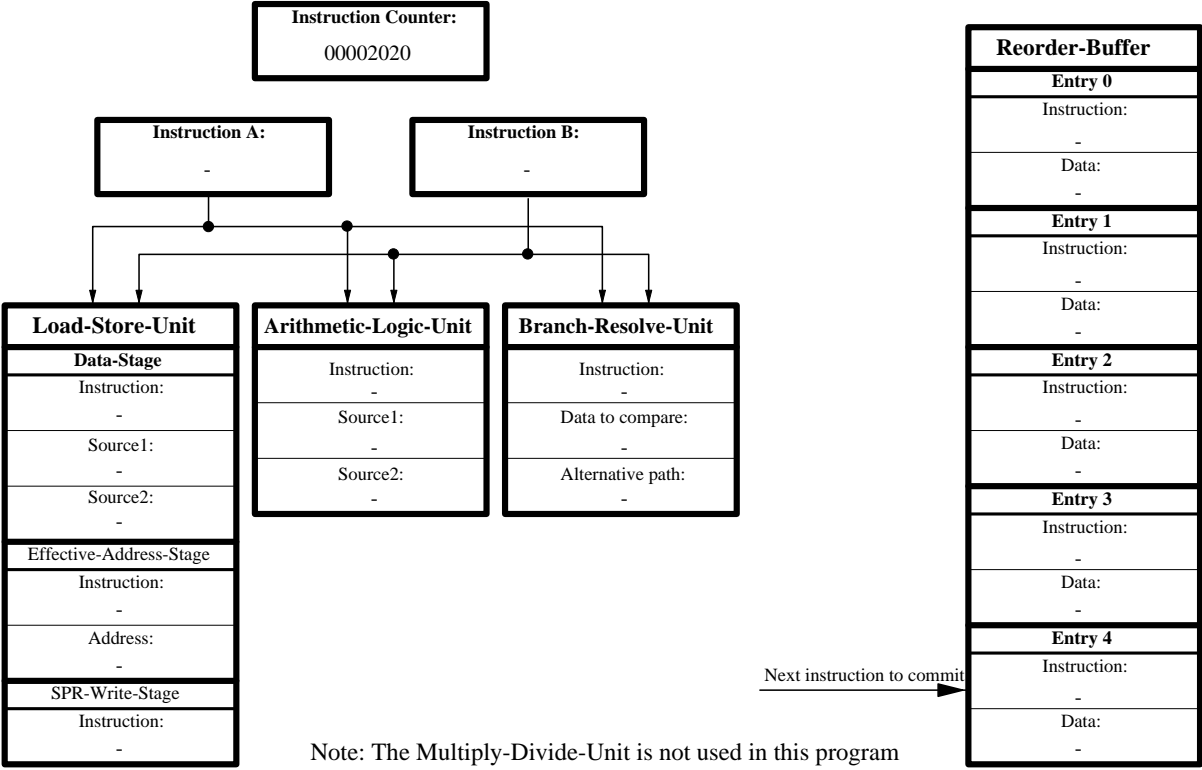
Clock: 36



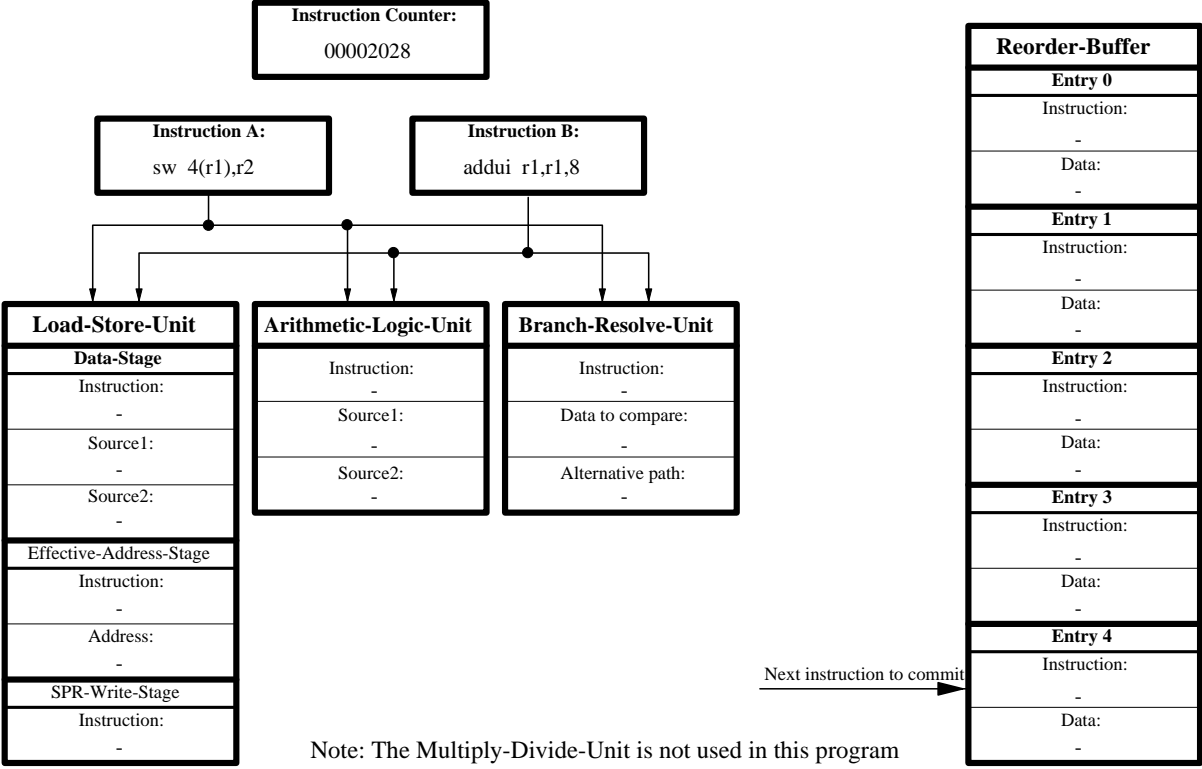
Clock: 37



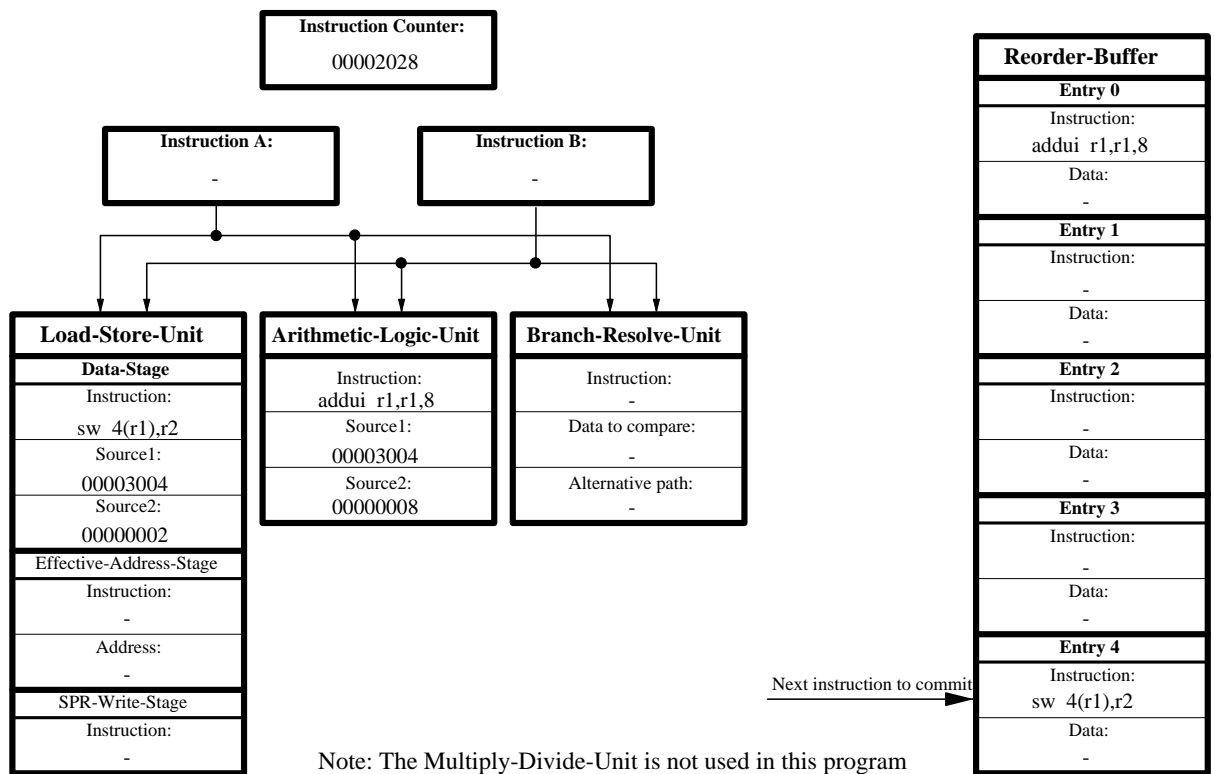
Clock: 38



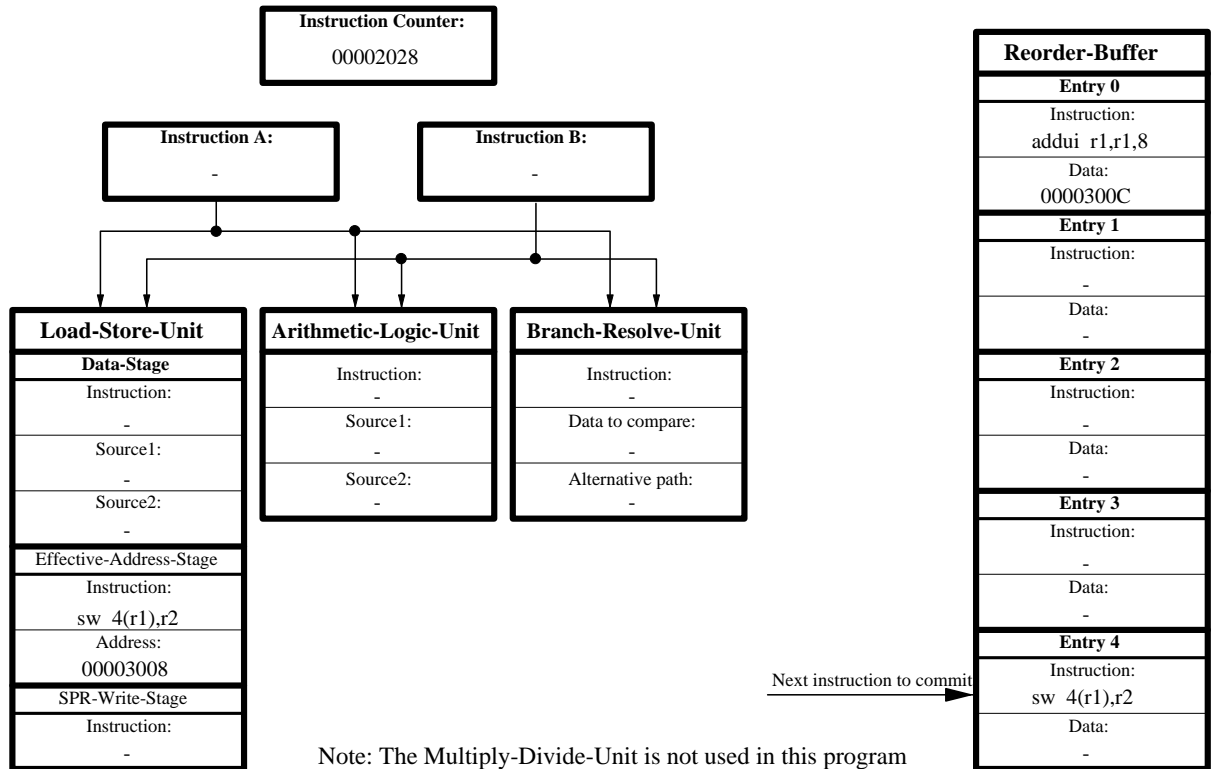
Clock: 39



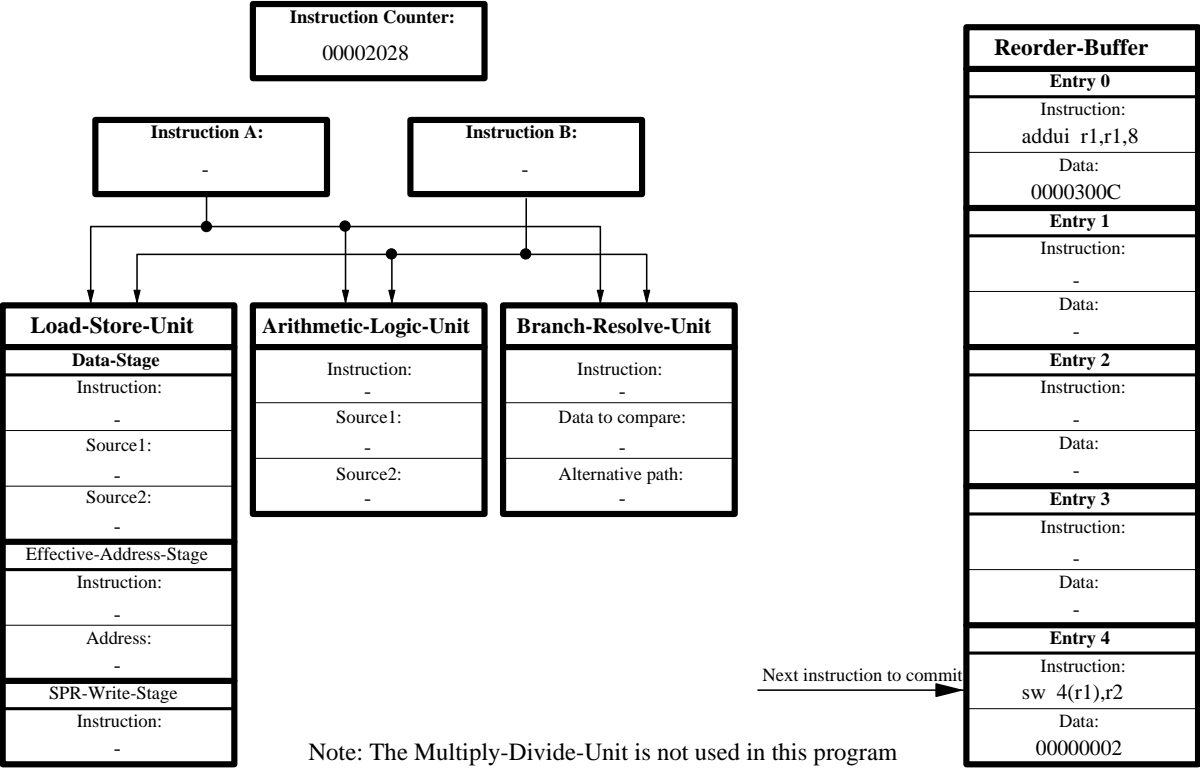
Clock: 40



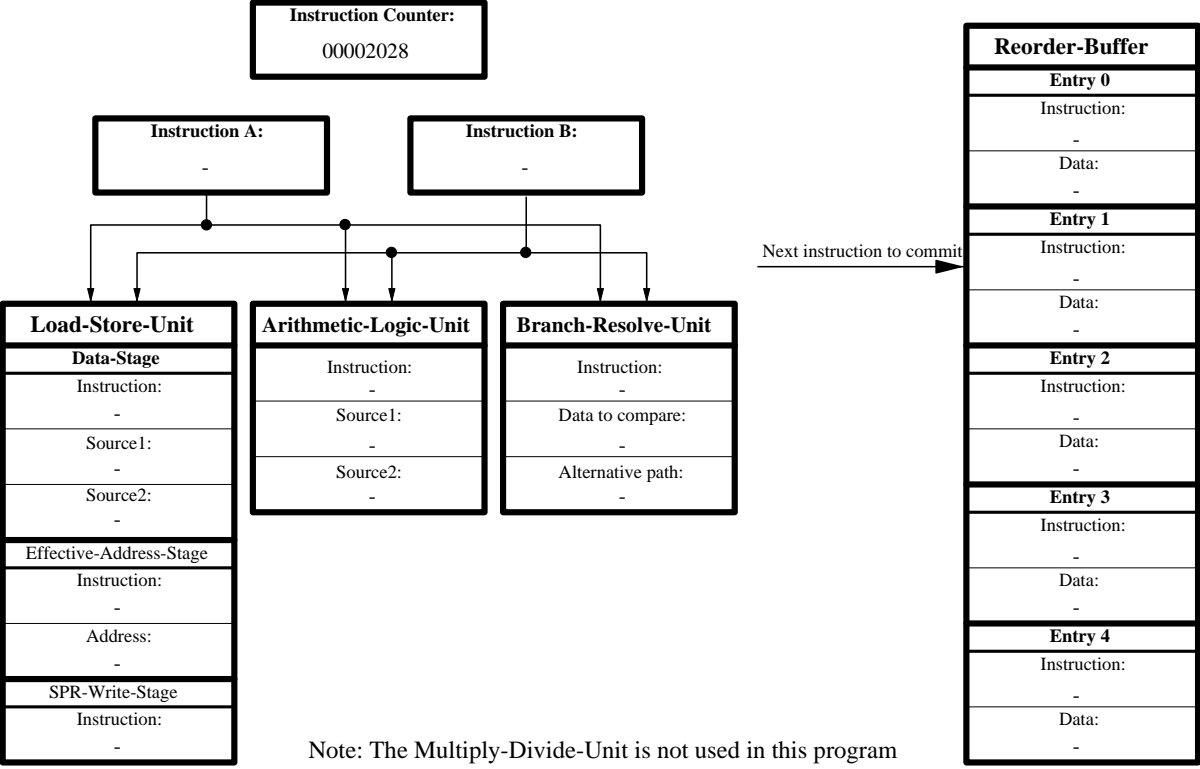
Clock: 41



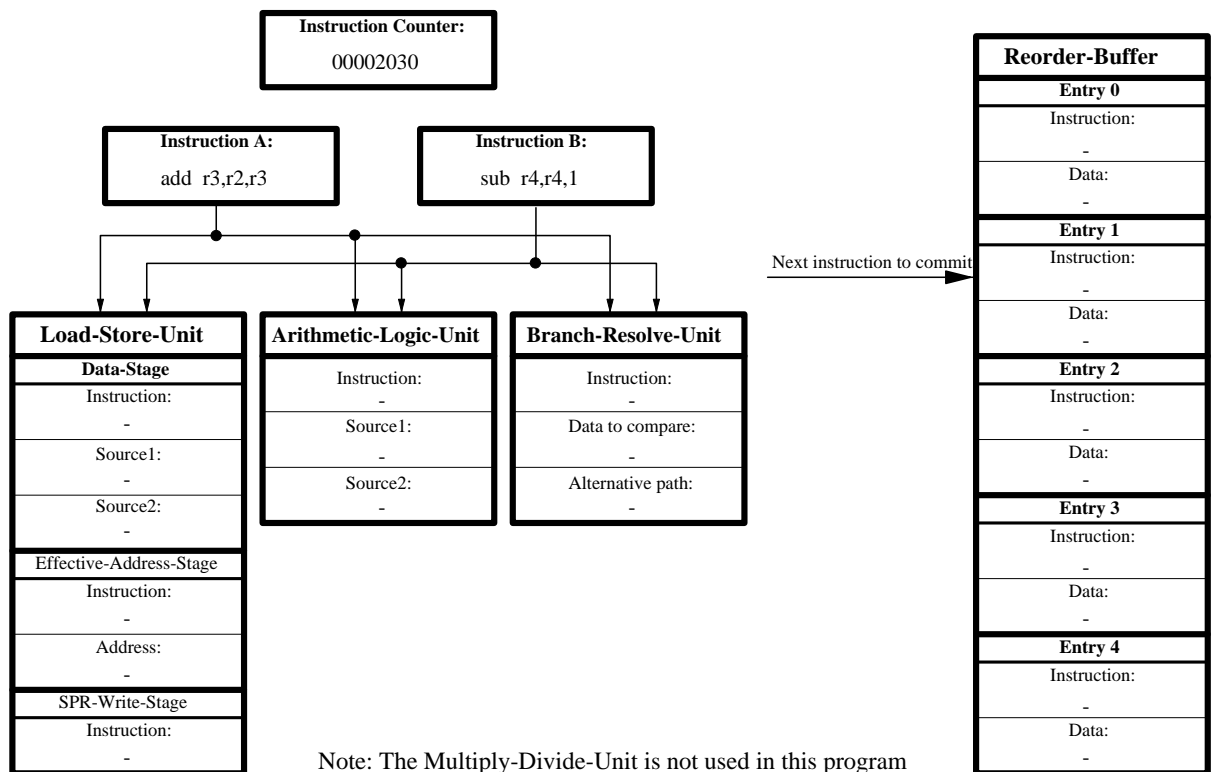
Clock: 42



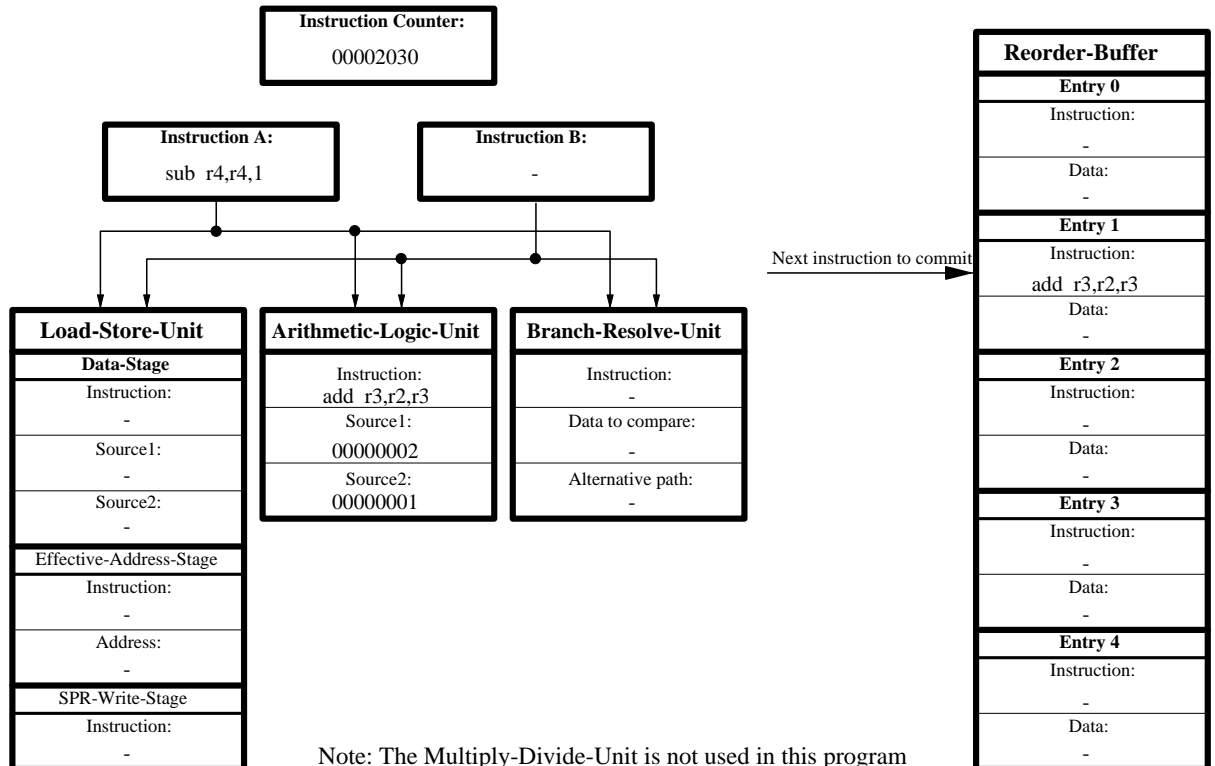
Clock: 43, 44, 45, 46



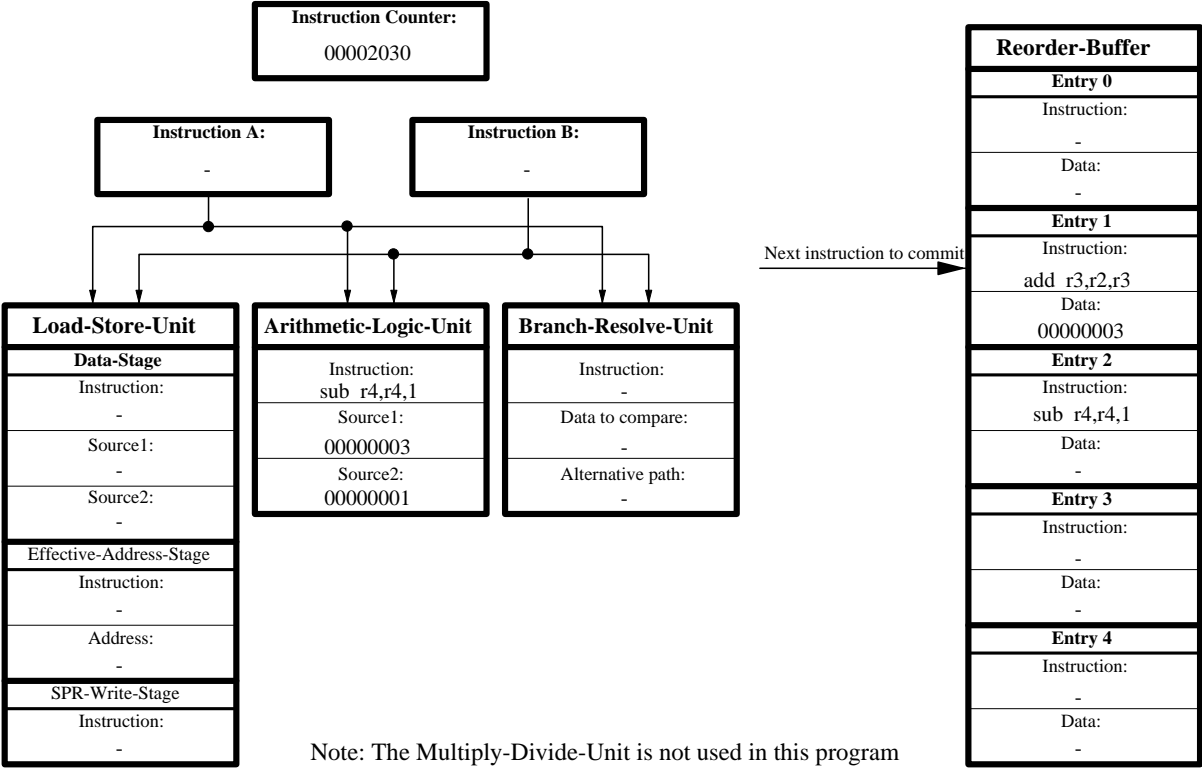
Clock: 47



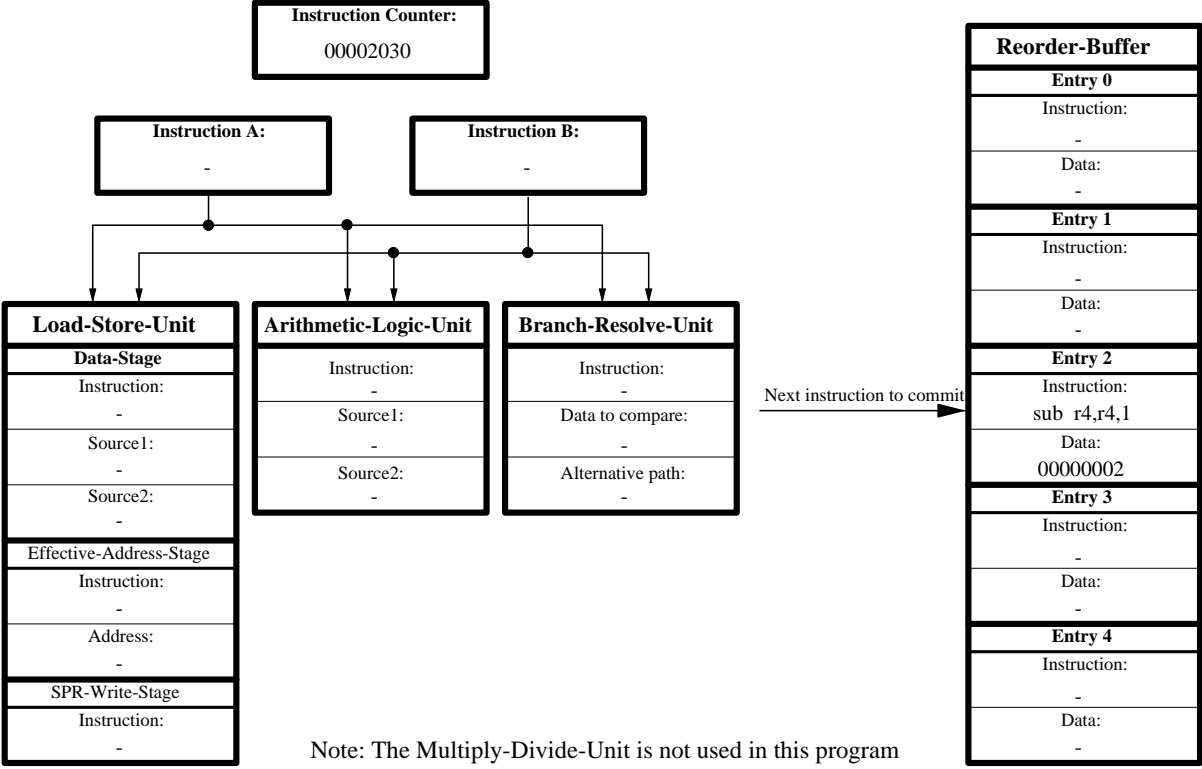
Clock: 48



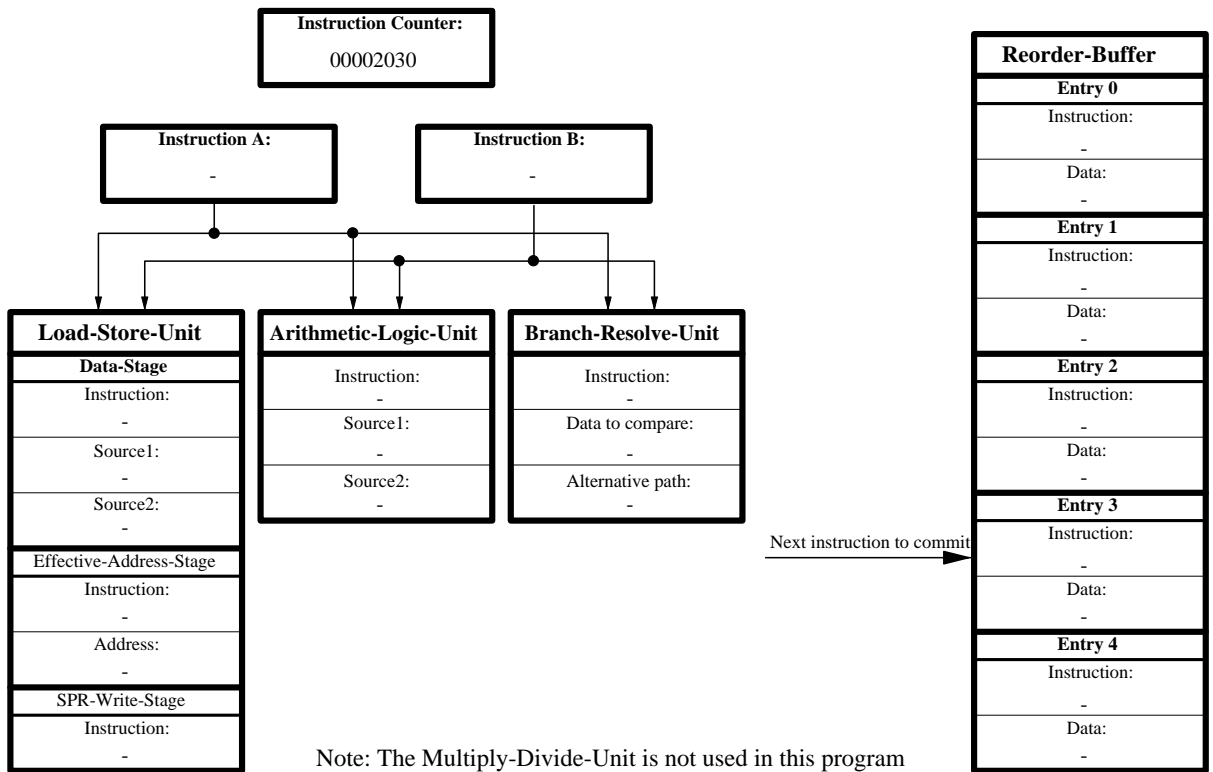
Clock: 49



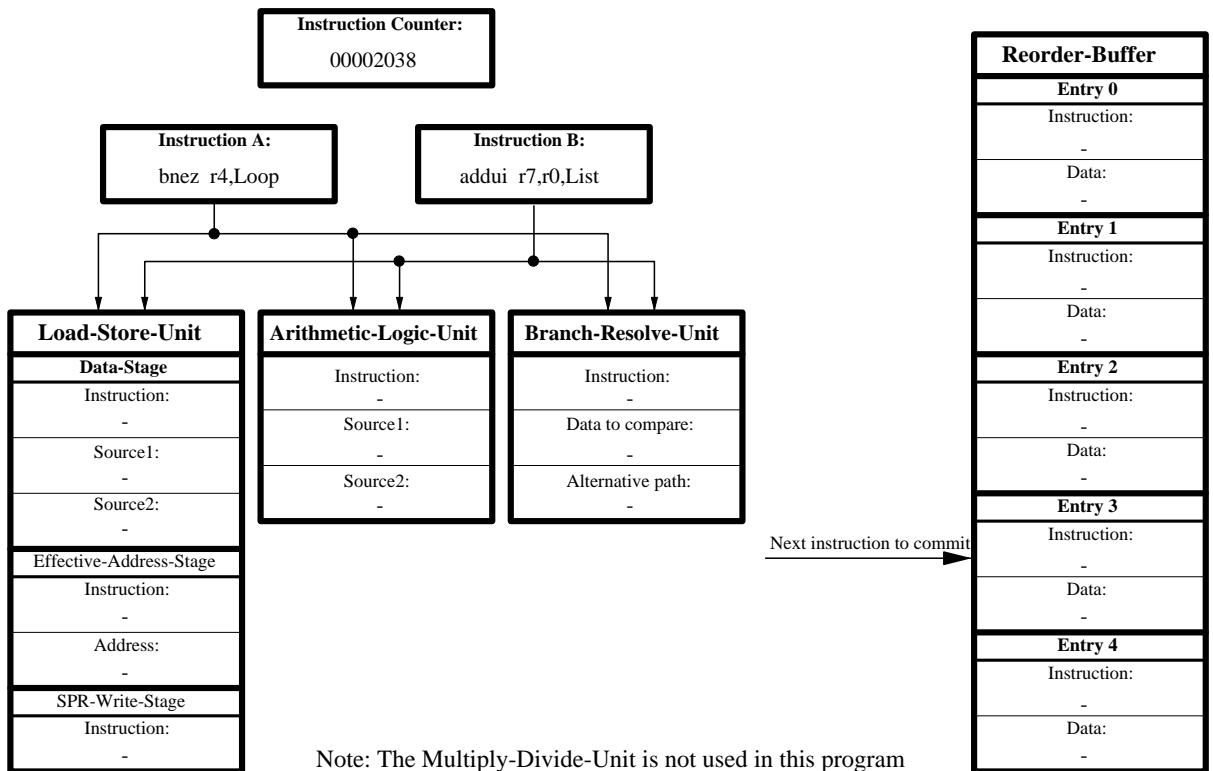
Clock: 50



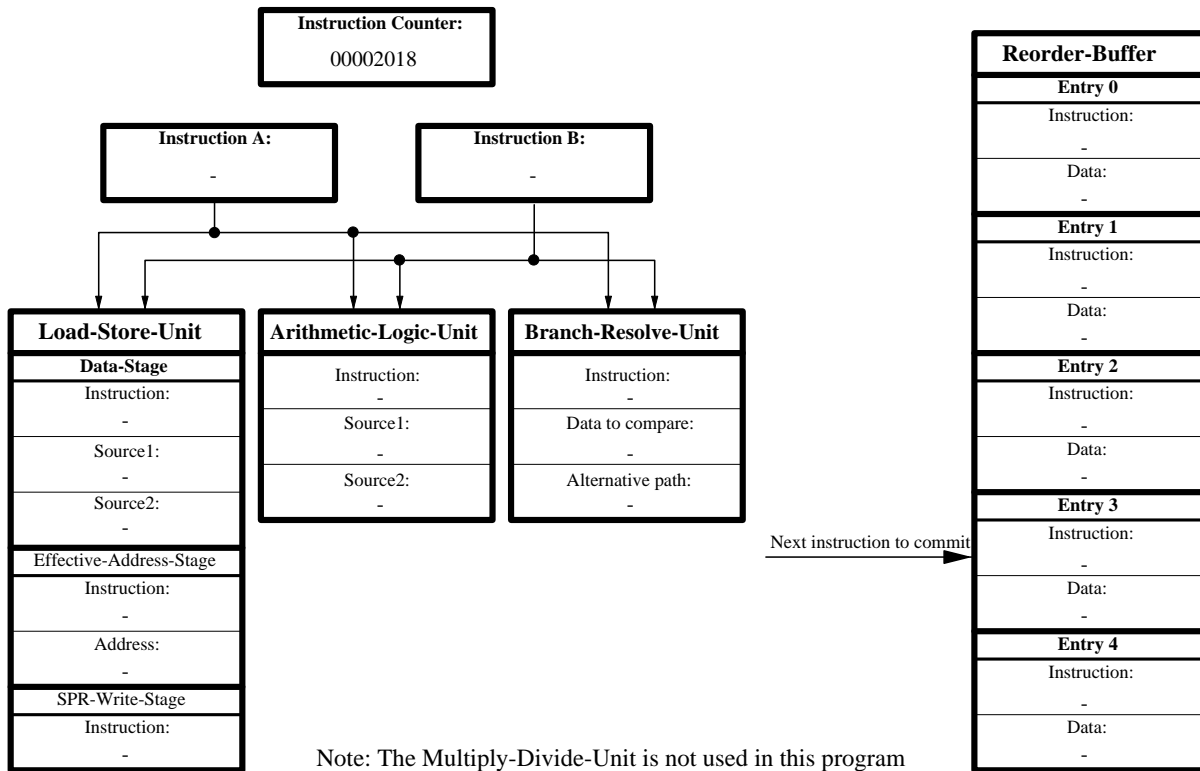
Clock: 51, 52, 53, 54



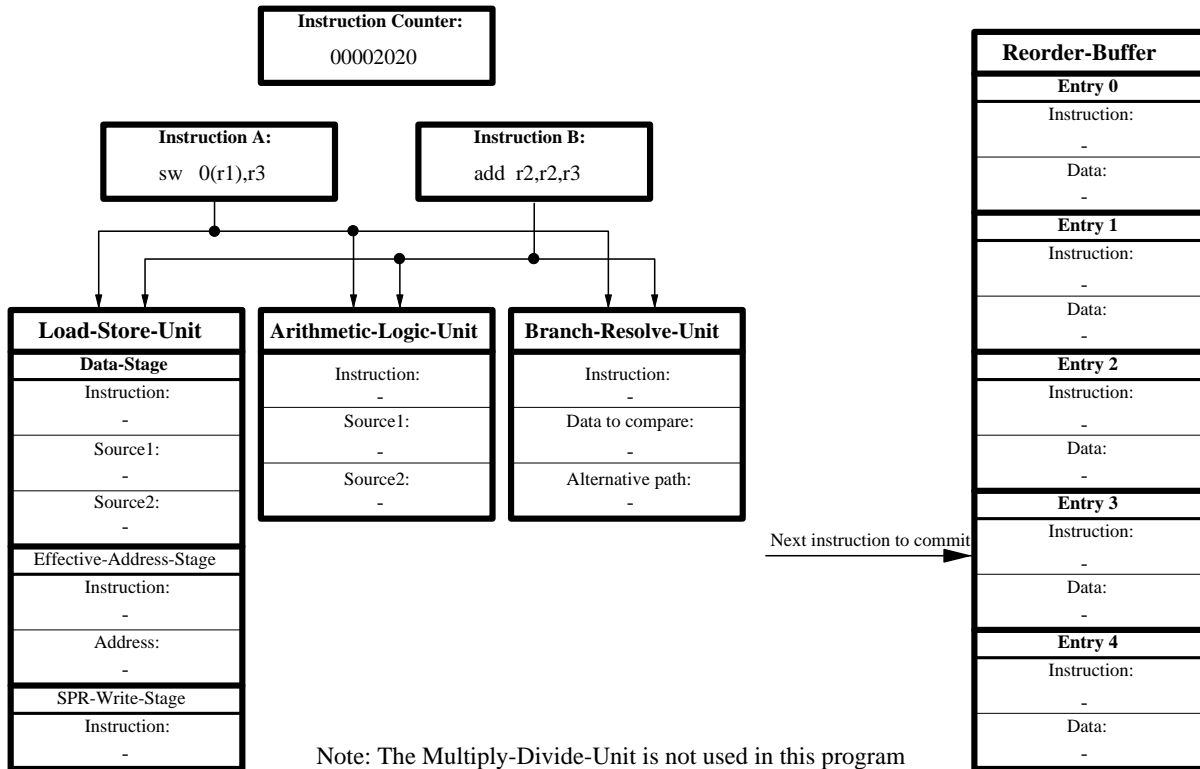
Clock: 55



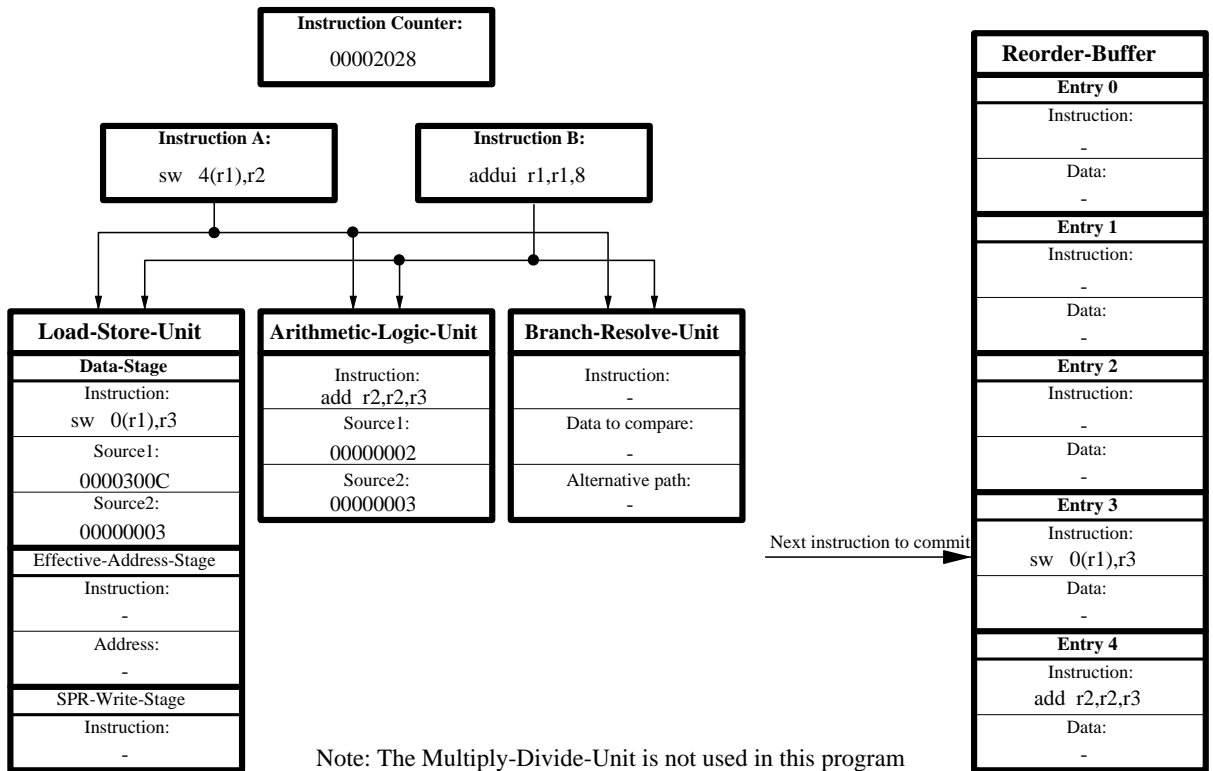
Clock: 56



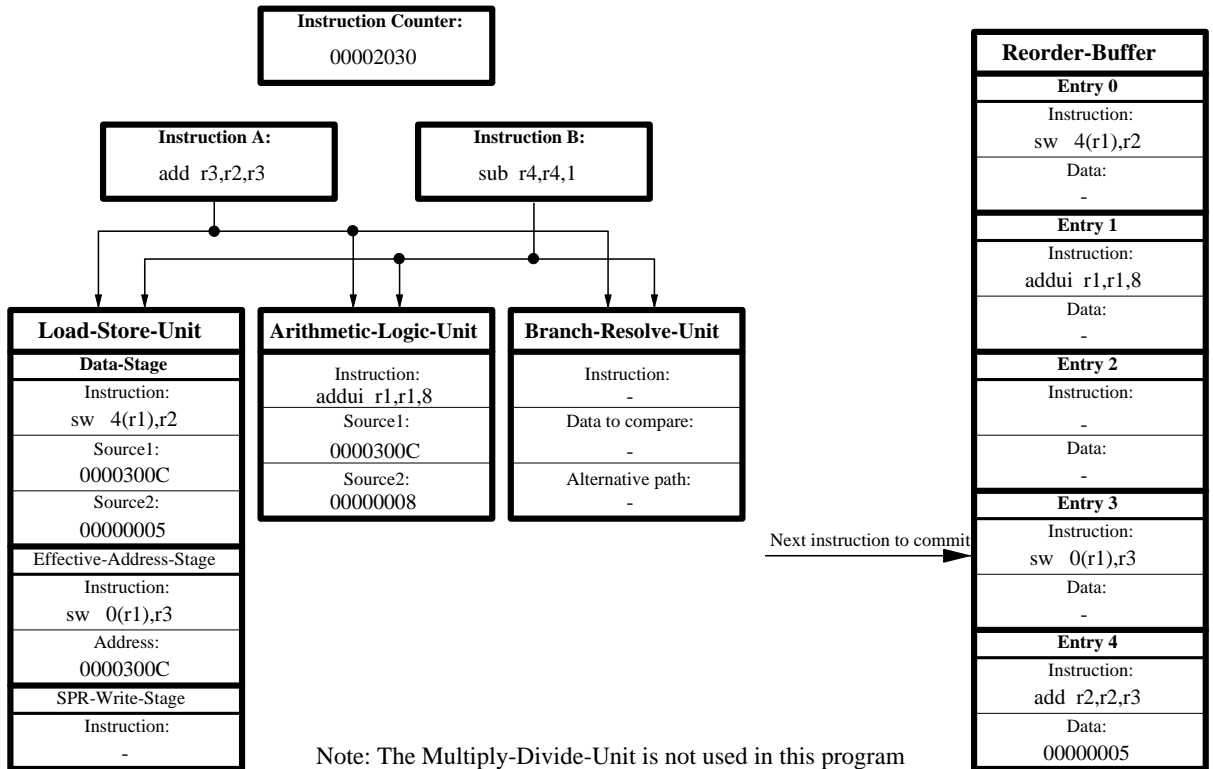
Clock: 57



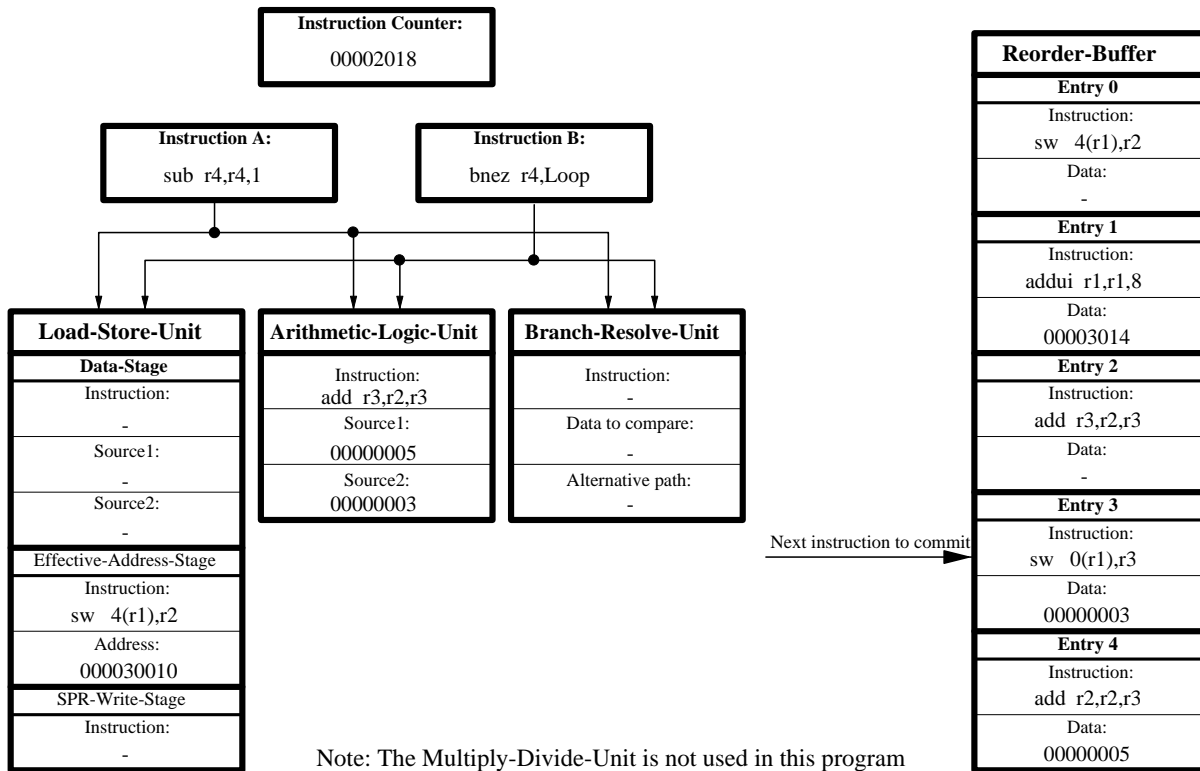
Clock: 58



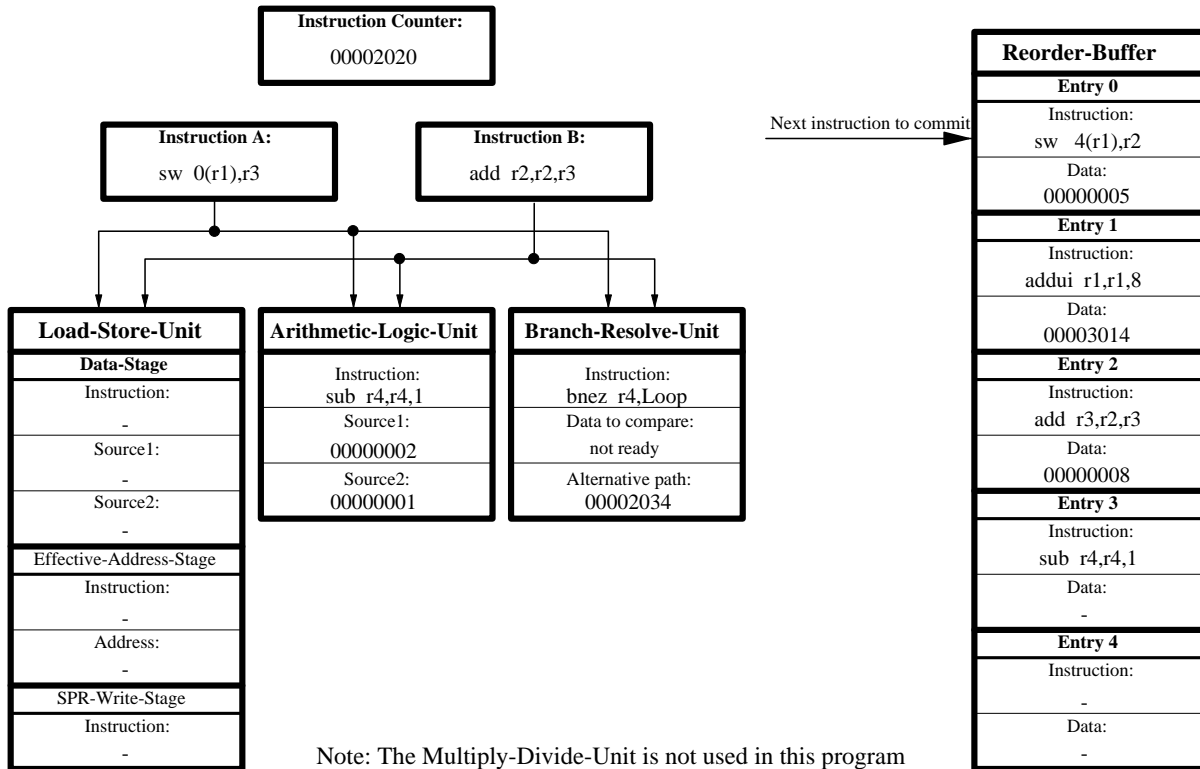
Clock: 59



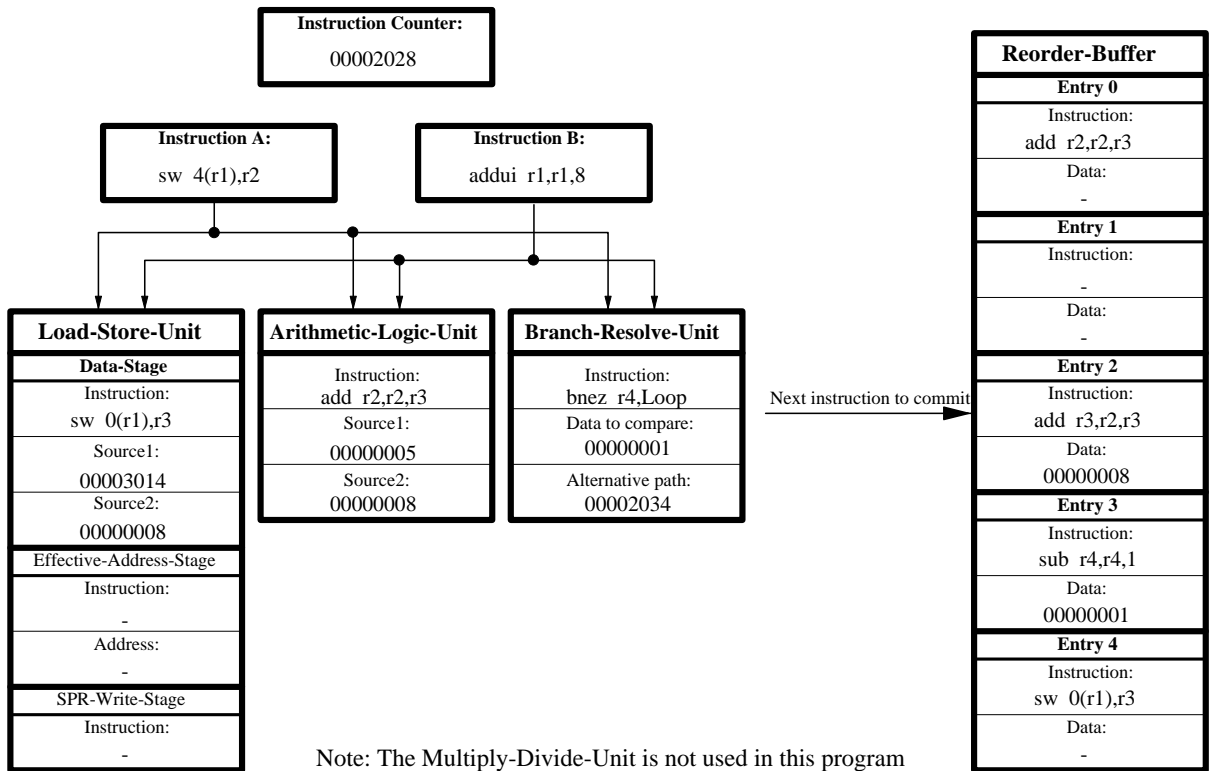
Clock: 60



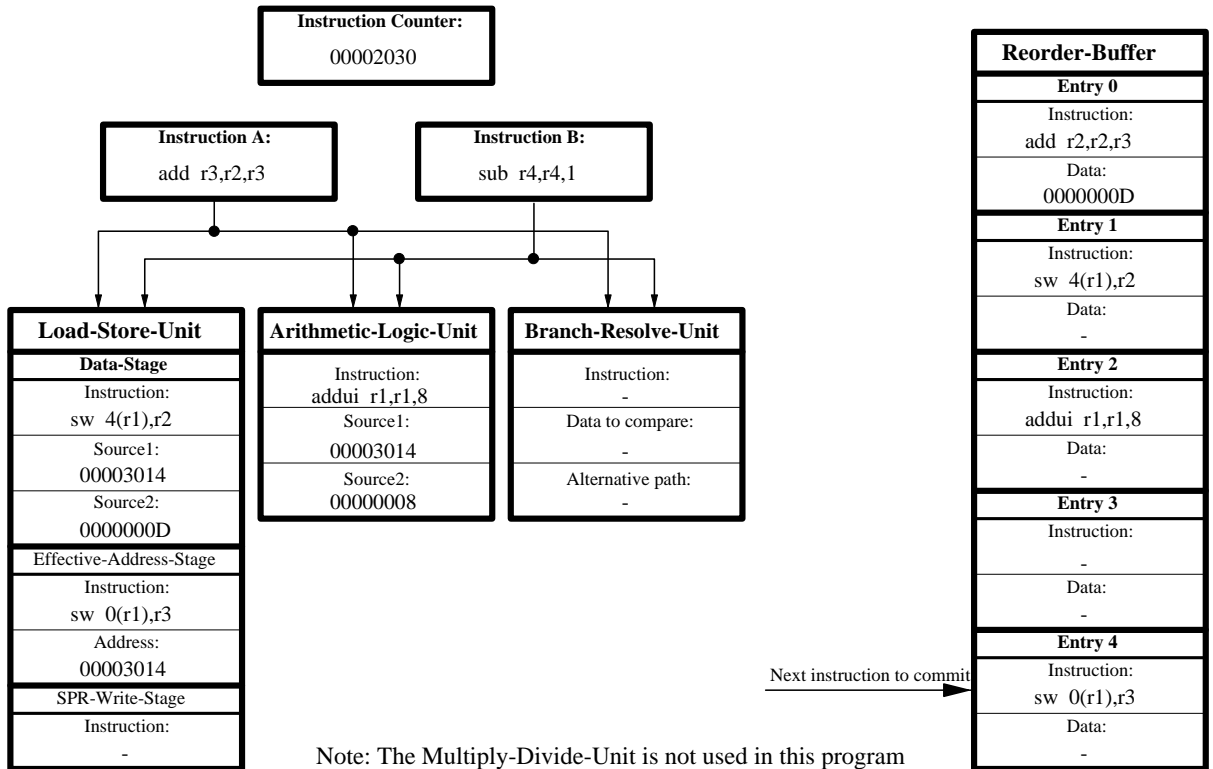
Clock: 61



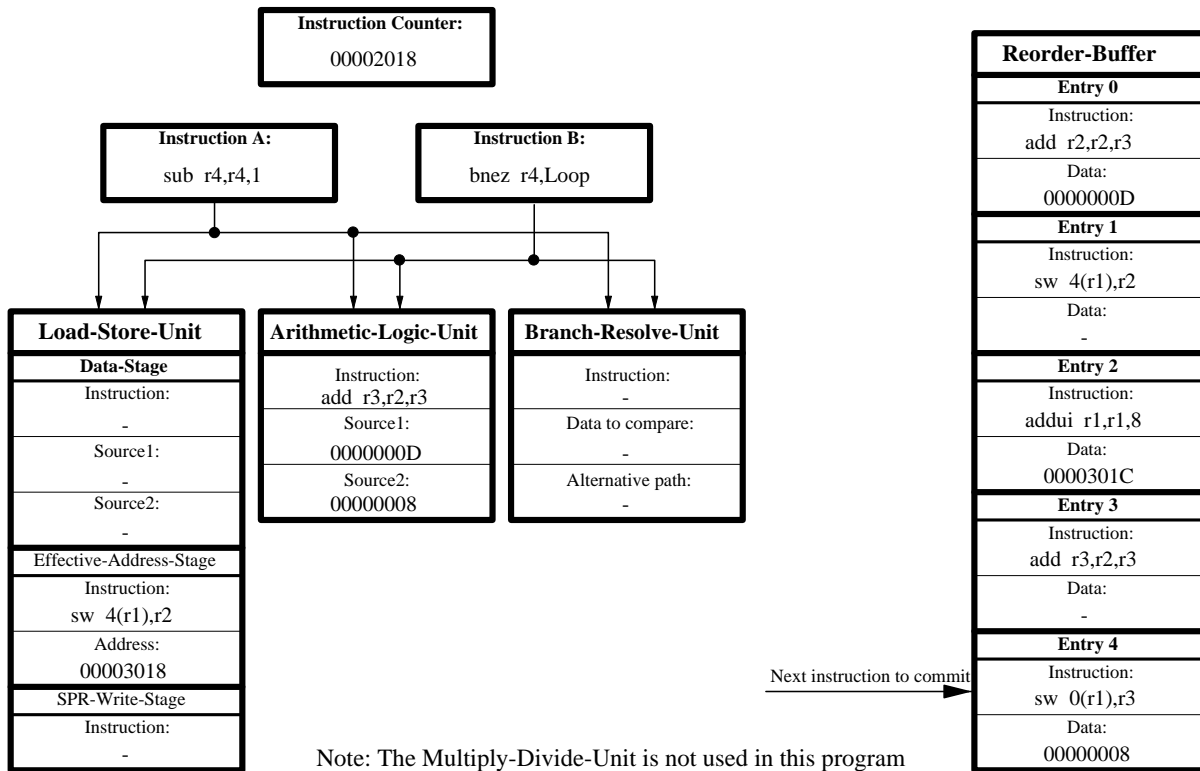
Clock: 62



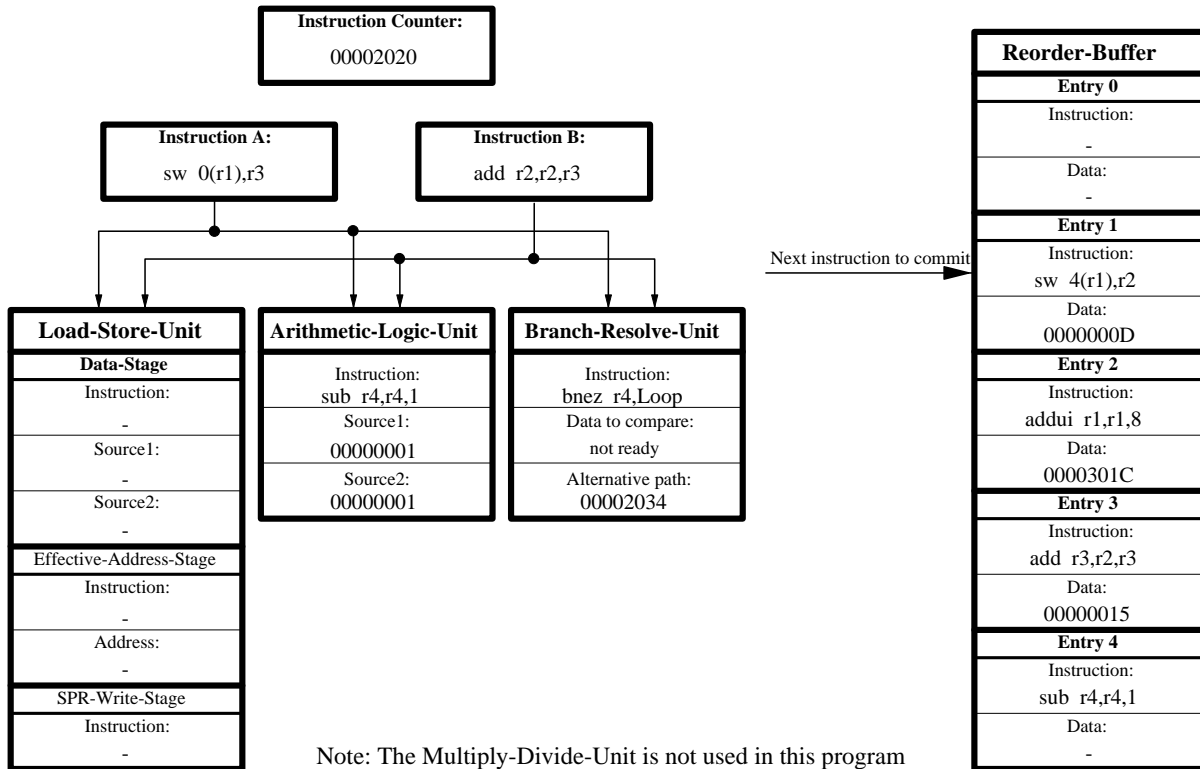
Clock: 63



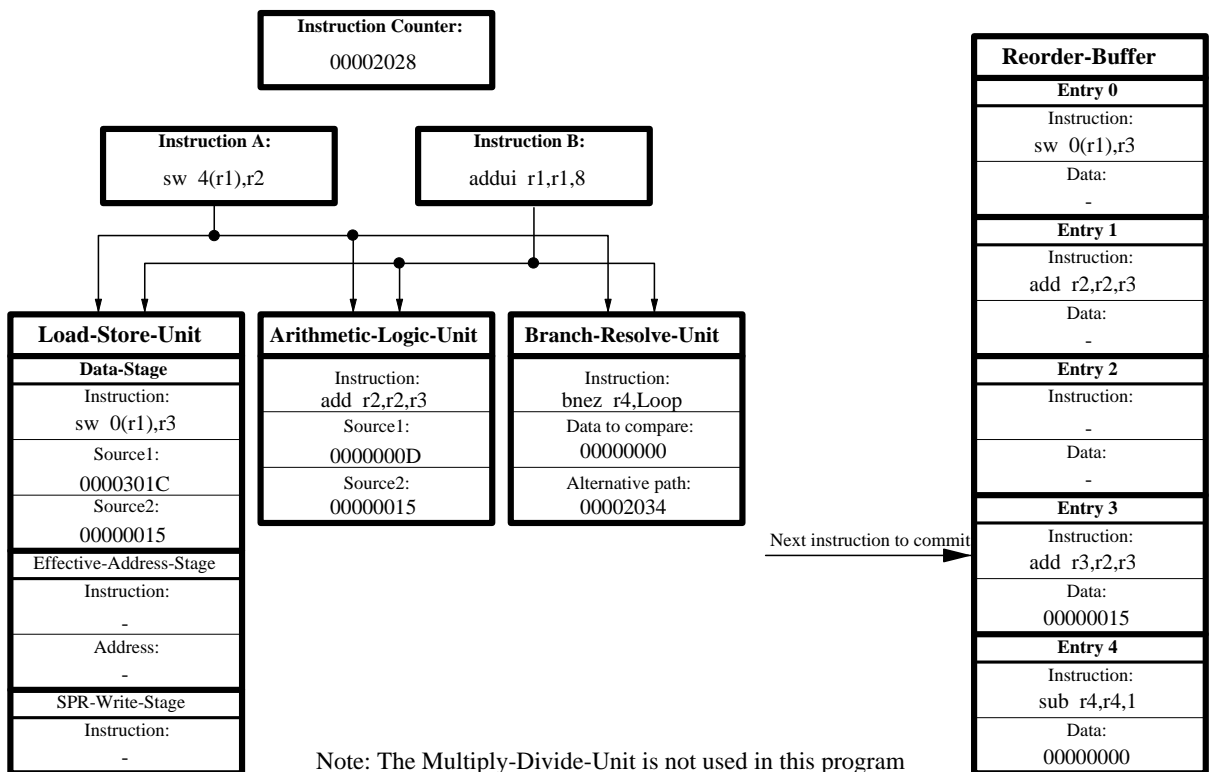
Clock: 64



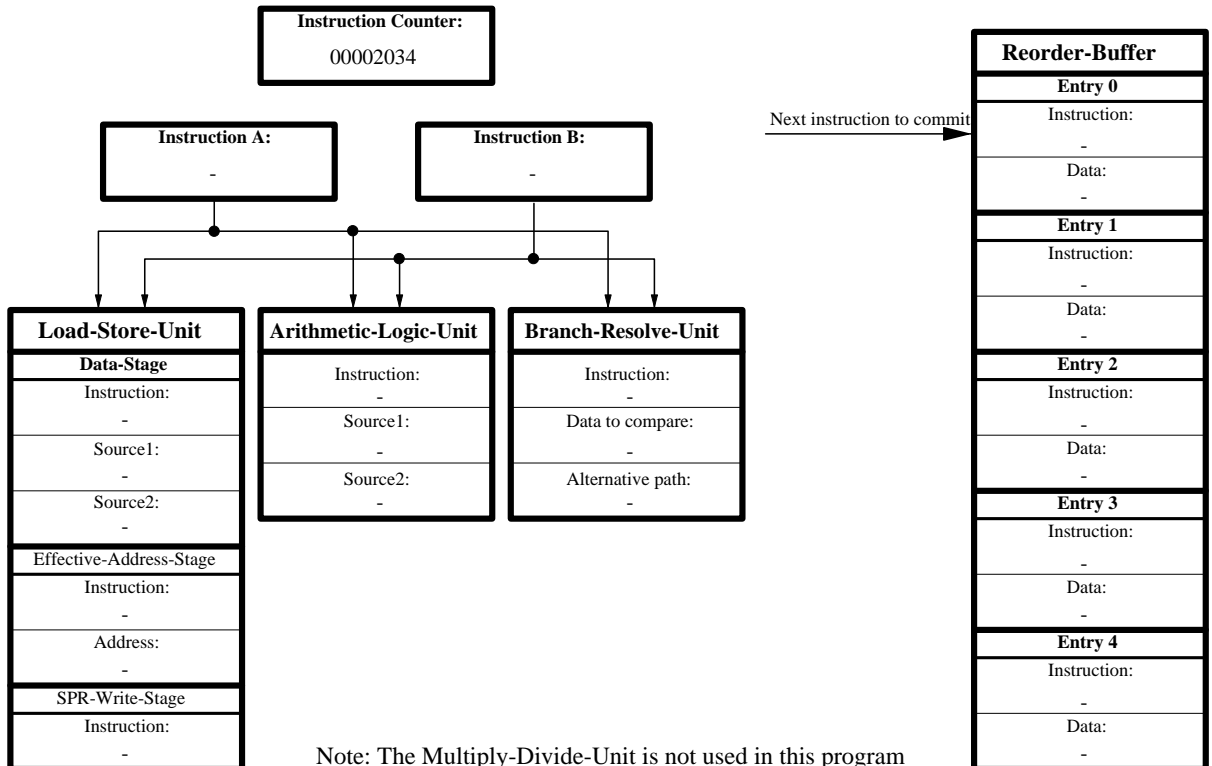
Clock: 65



Clock: 66



Clock: 67



Der weitere Befehlsfluß durch die Pipeline wird nur durch die tabellarischen Darstellungen dokumentiert.